

Fig 1

Transmit 201

Receive 202

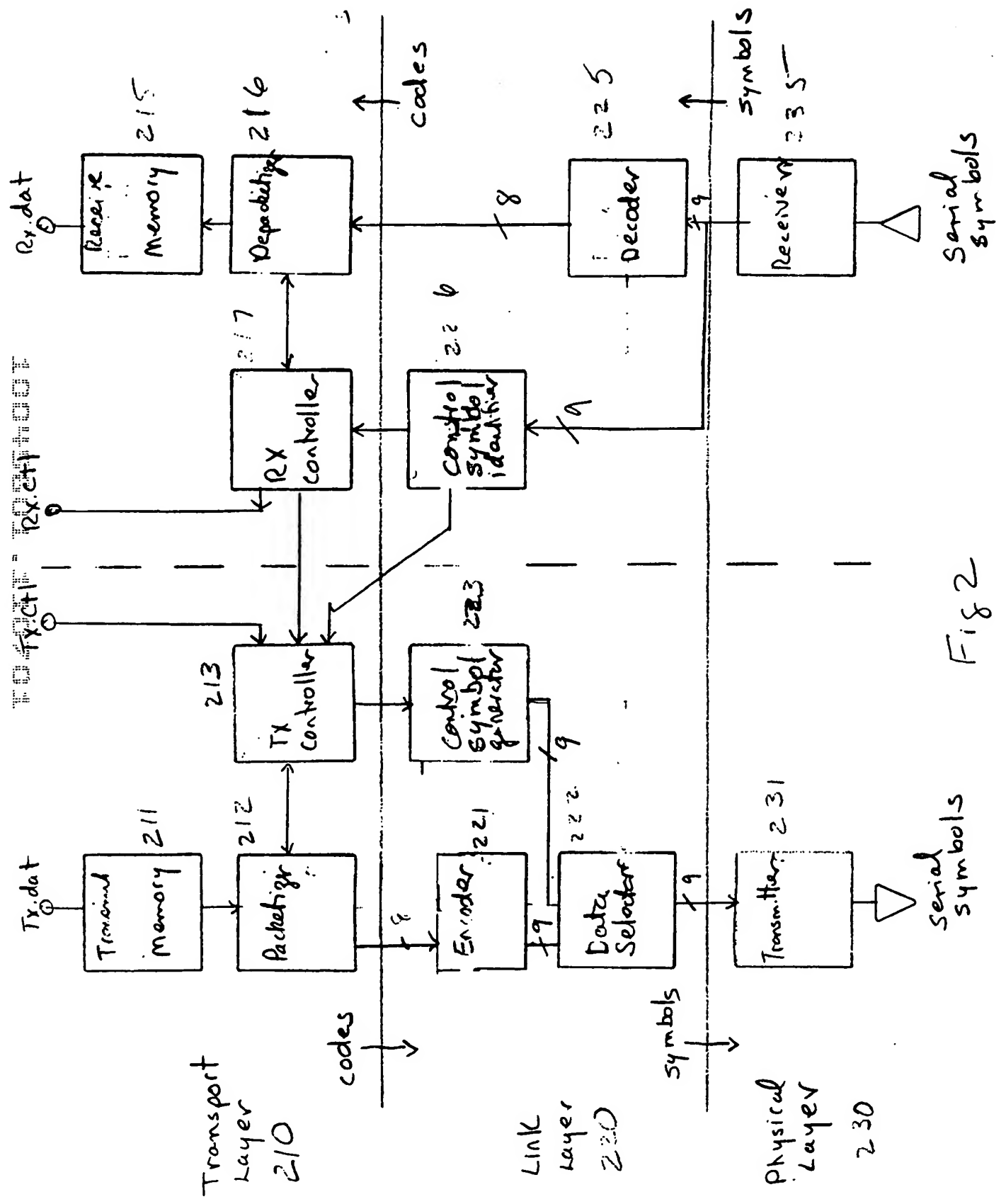


Fig 2

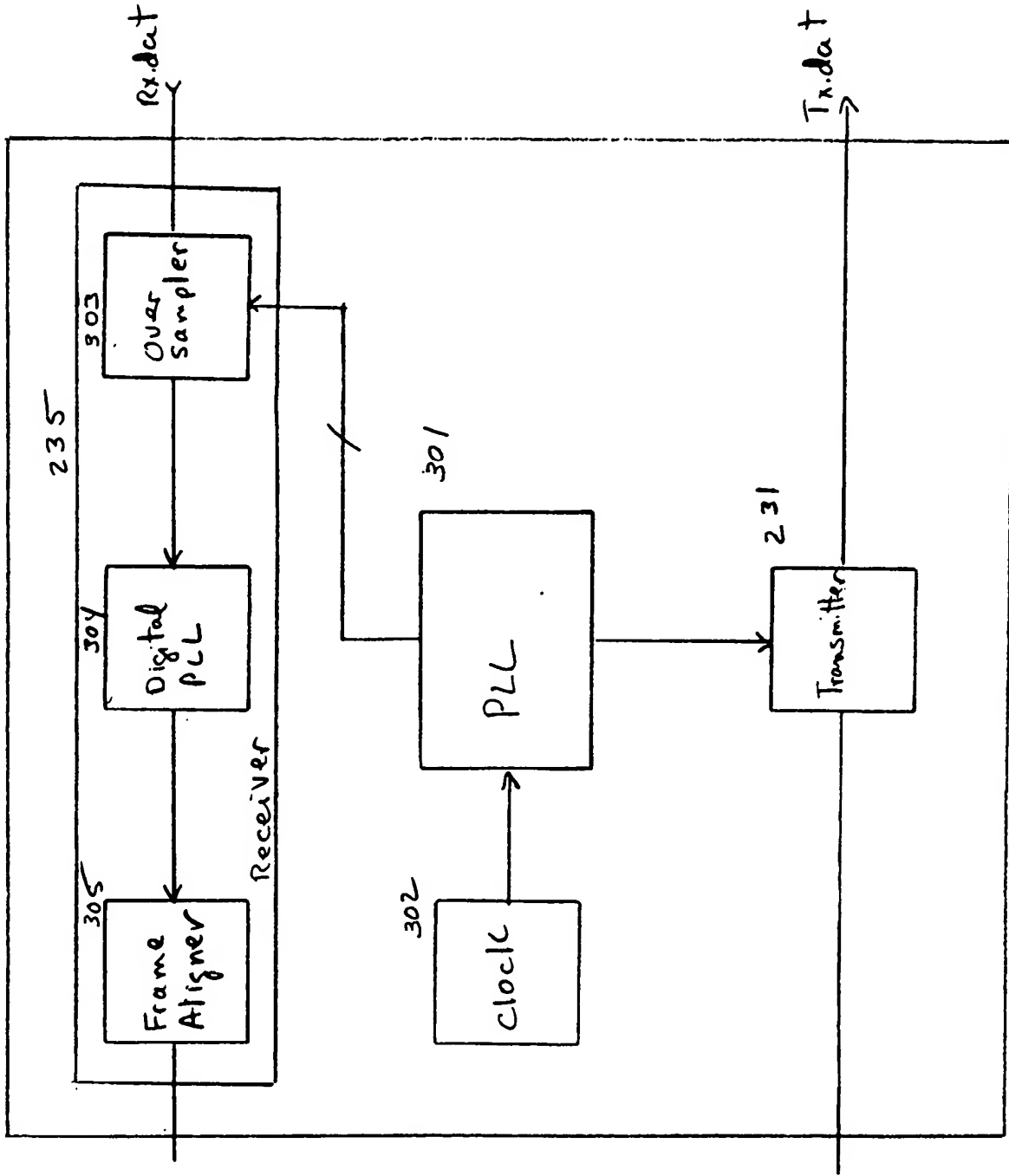


Fig 3

Packet

400

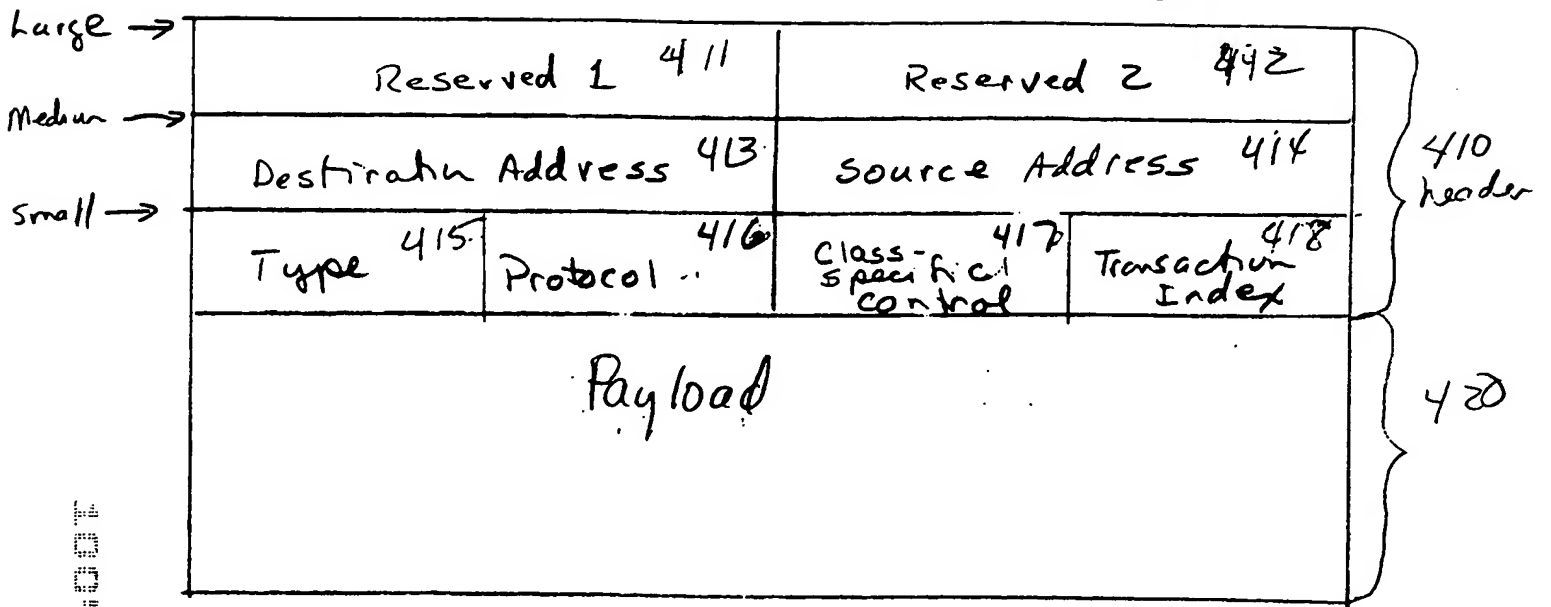
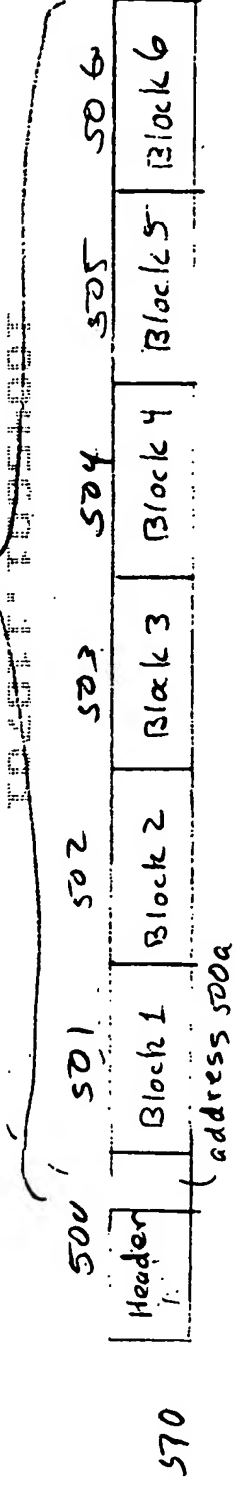


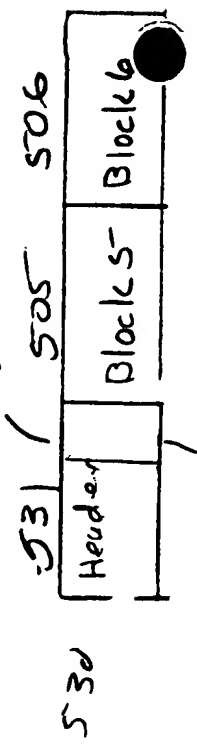
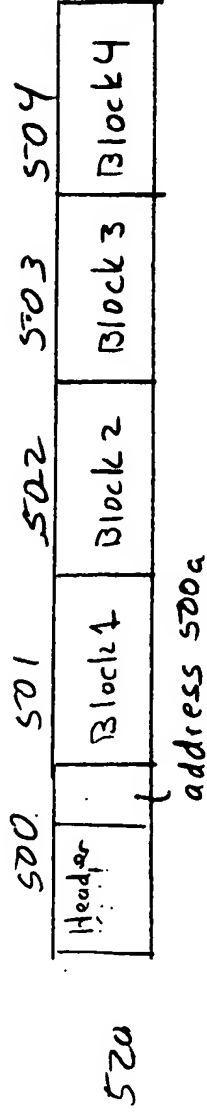
Fig 4

100456789101112131415161718192021222324252627282930313233343536373839404142434445464748495051525354555657585960616263646566676869707172737475767778798081828384858687888990919293949596979899100

payload 511

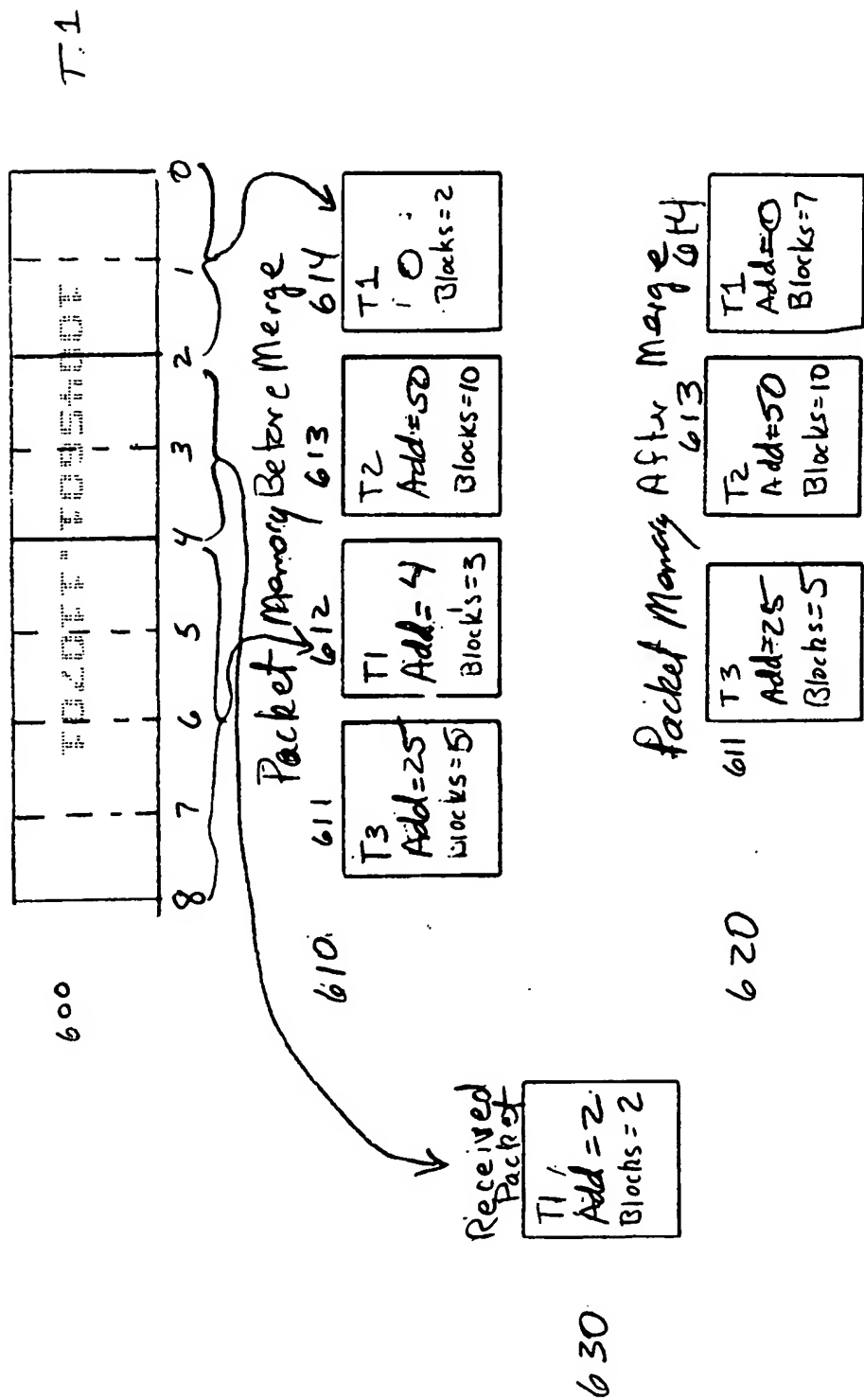


531a



address + 4

F. 45



F. 86

10045604 - 10704

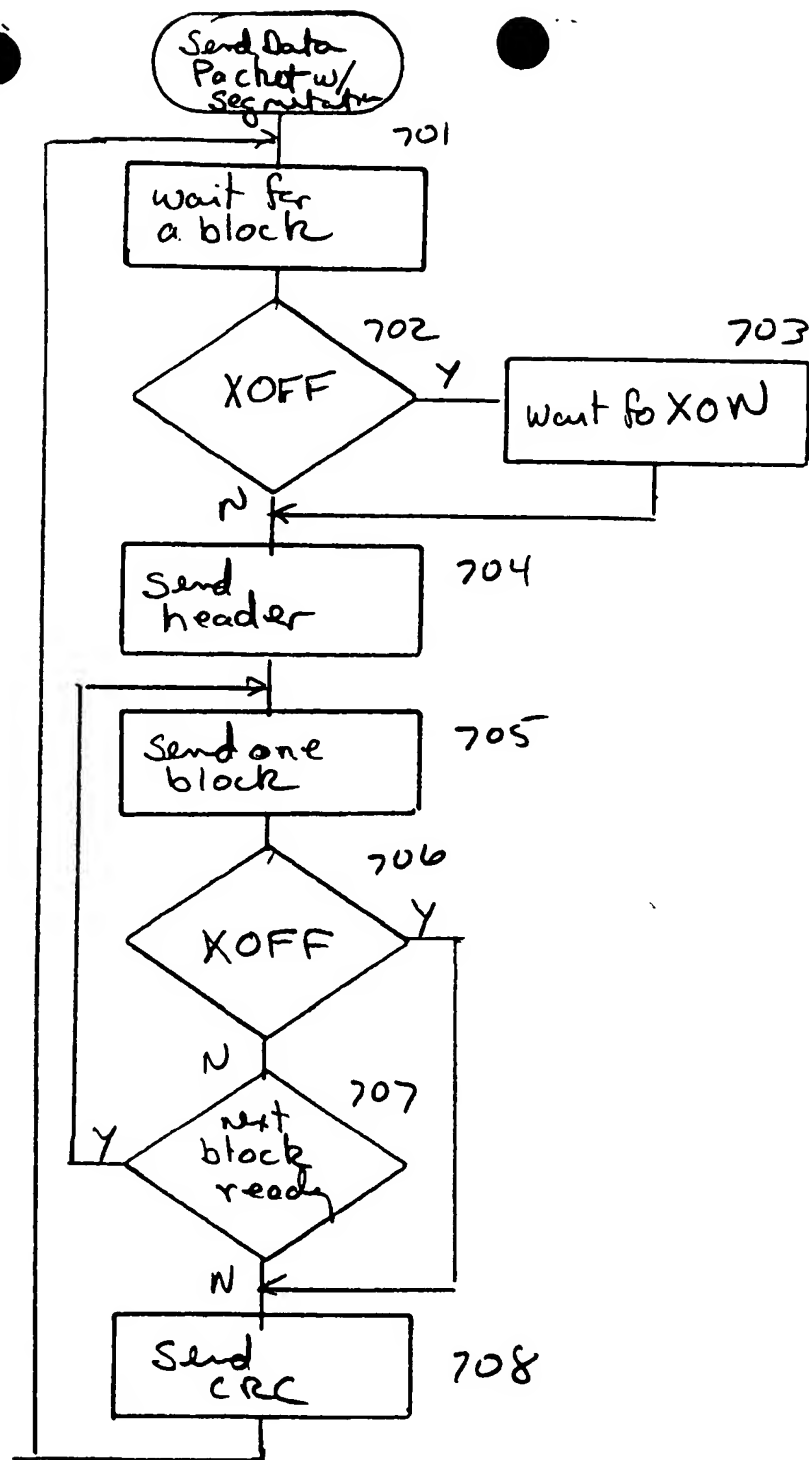


Fig 7

10045604.140704

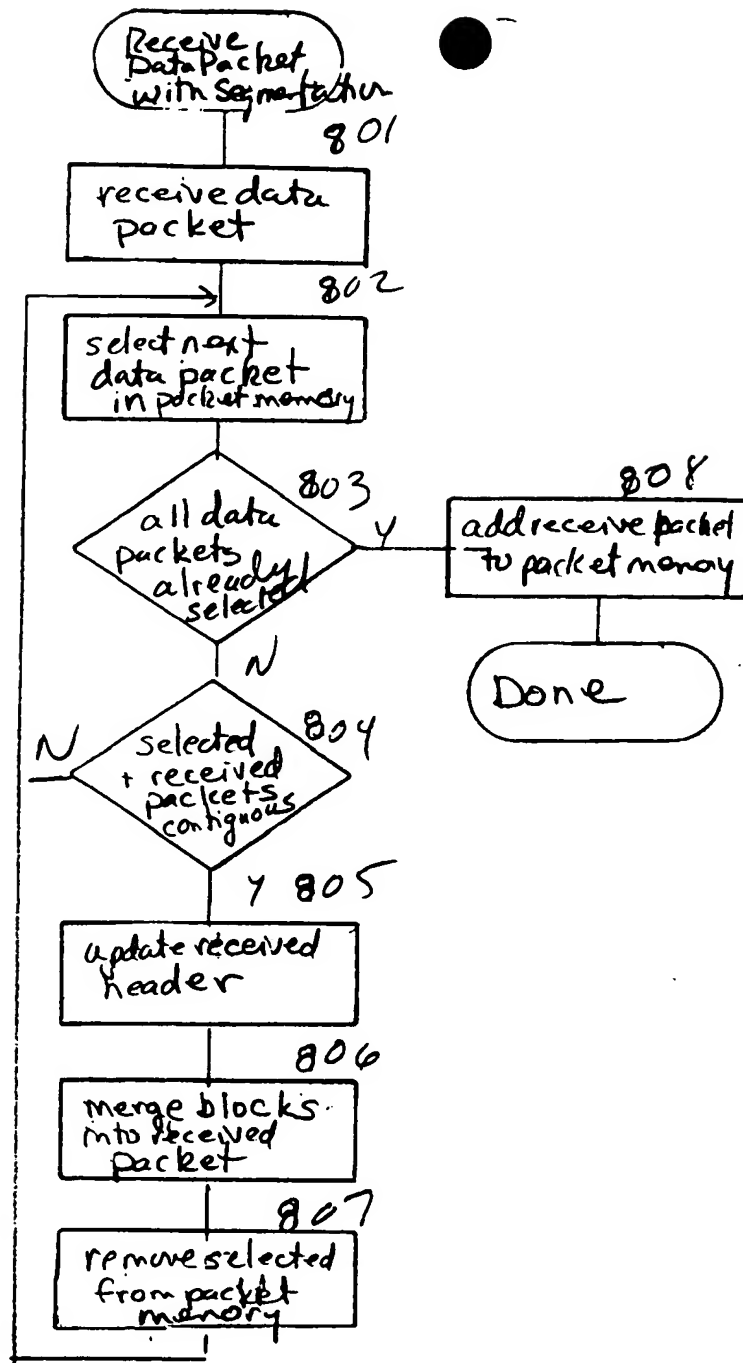
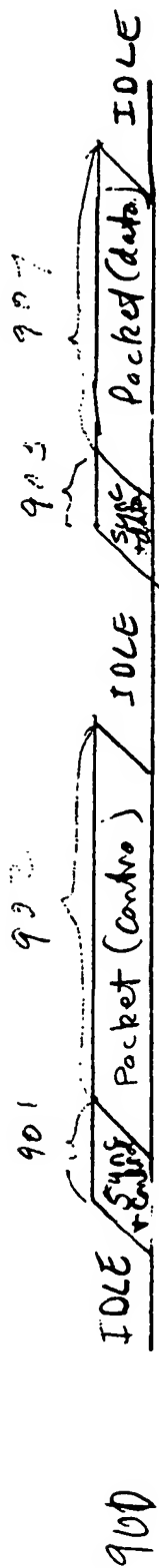


Fig 8

1045634.410701



Sync + packet type

Fig 9A

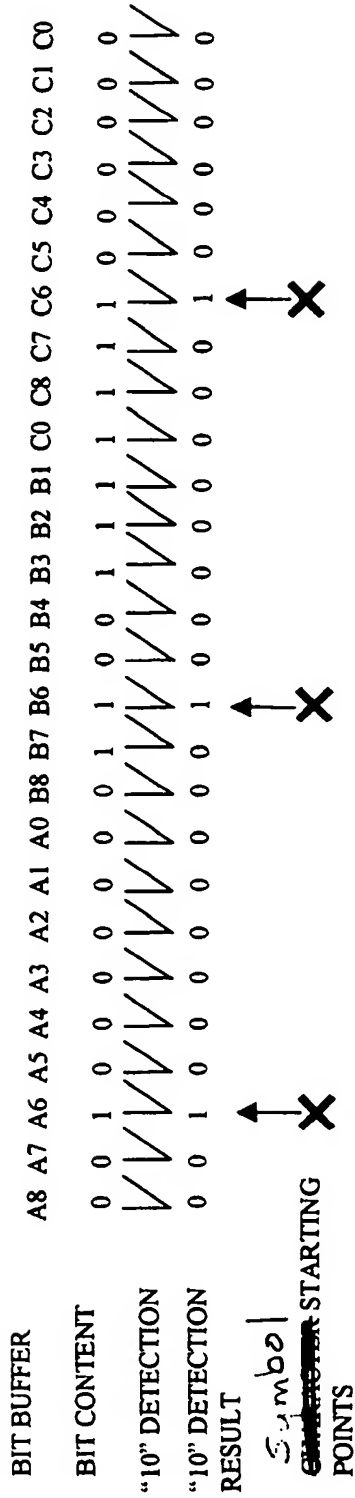


FIG.10

Fig 9B

910

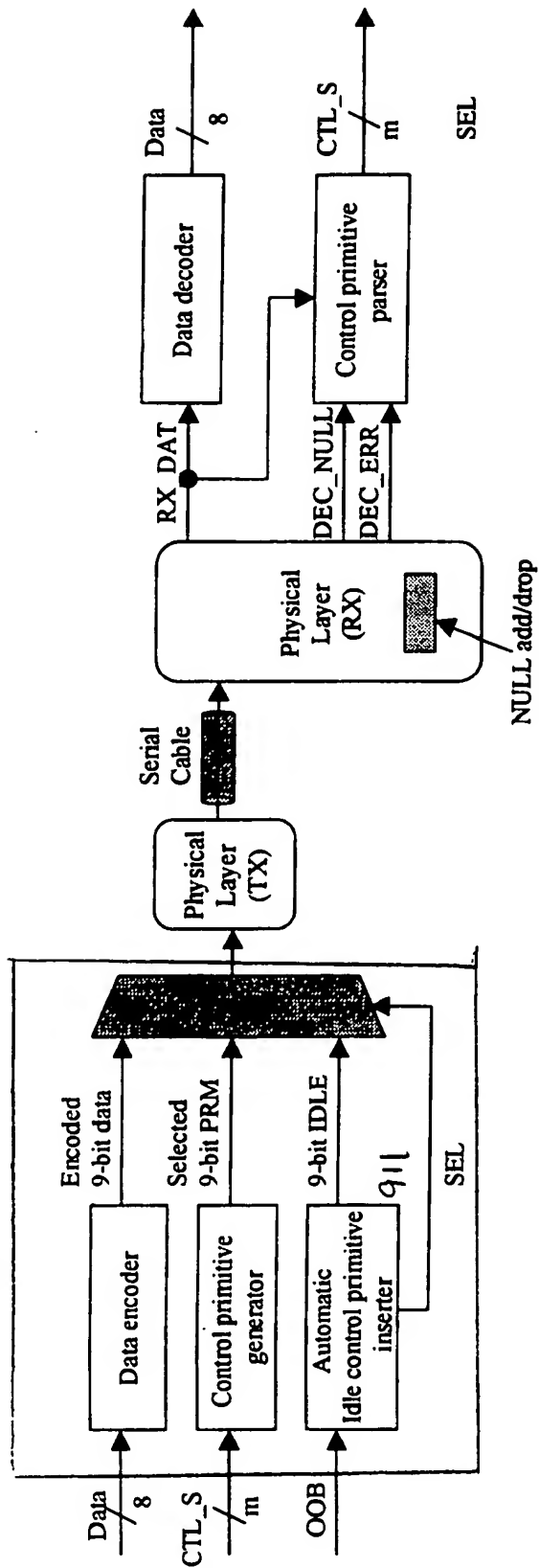


Fig. 9C

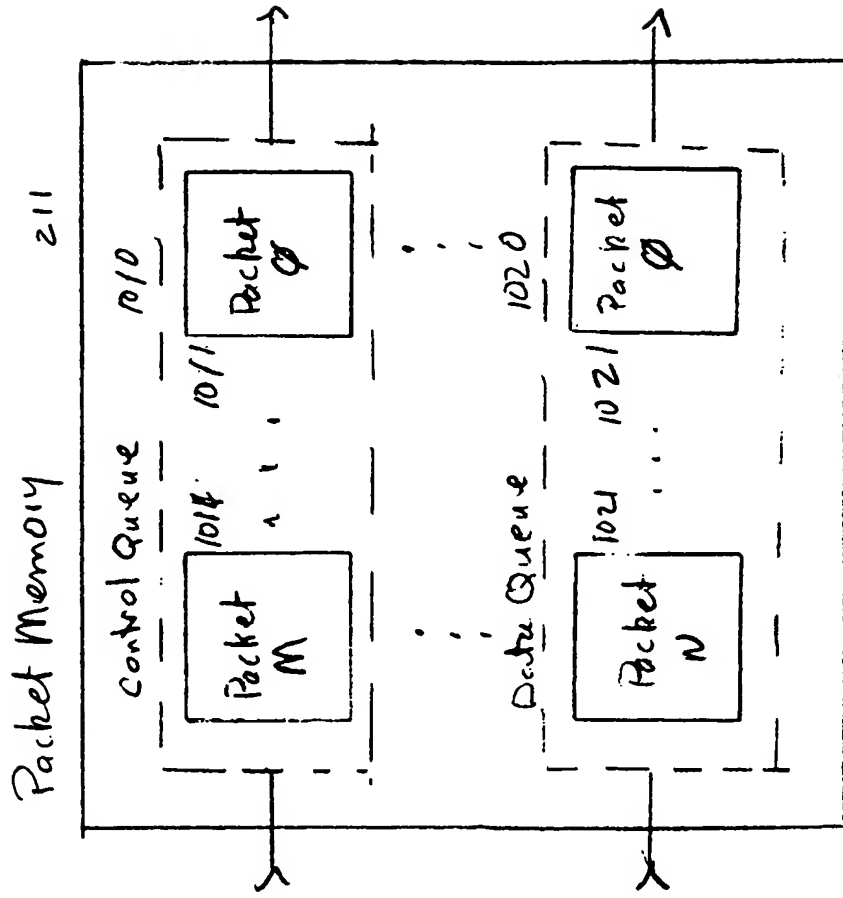


Fig 10

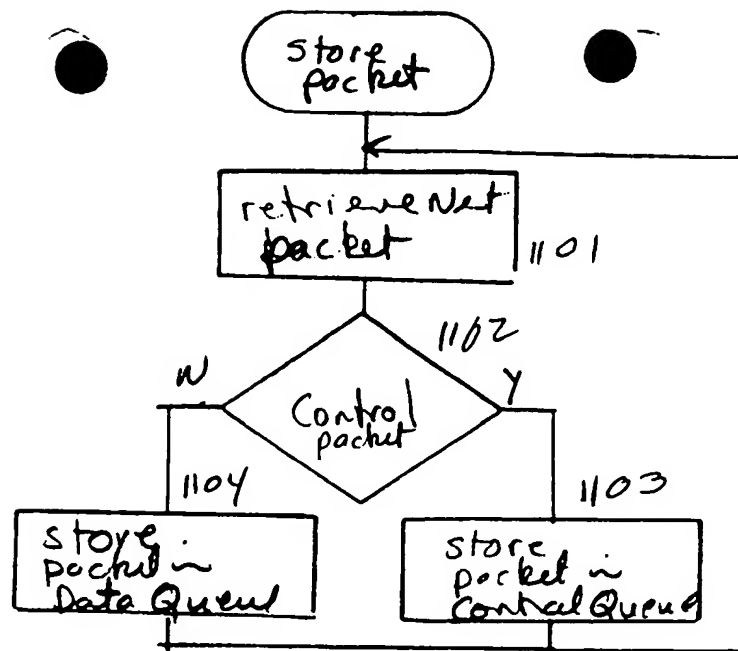


Fig 11

10045604 340704
 10045604 340704

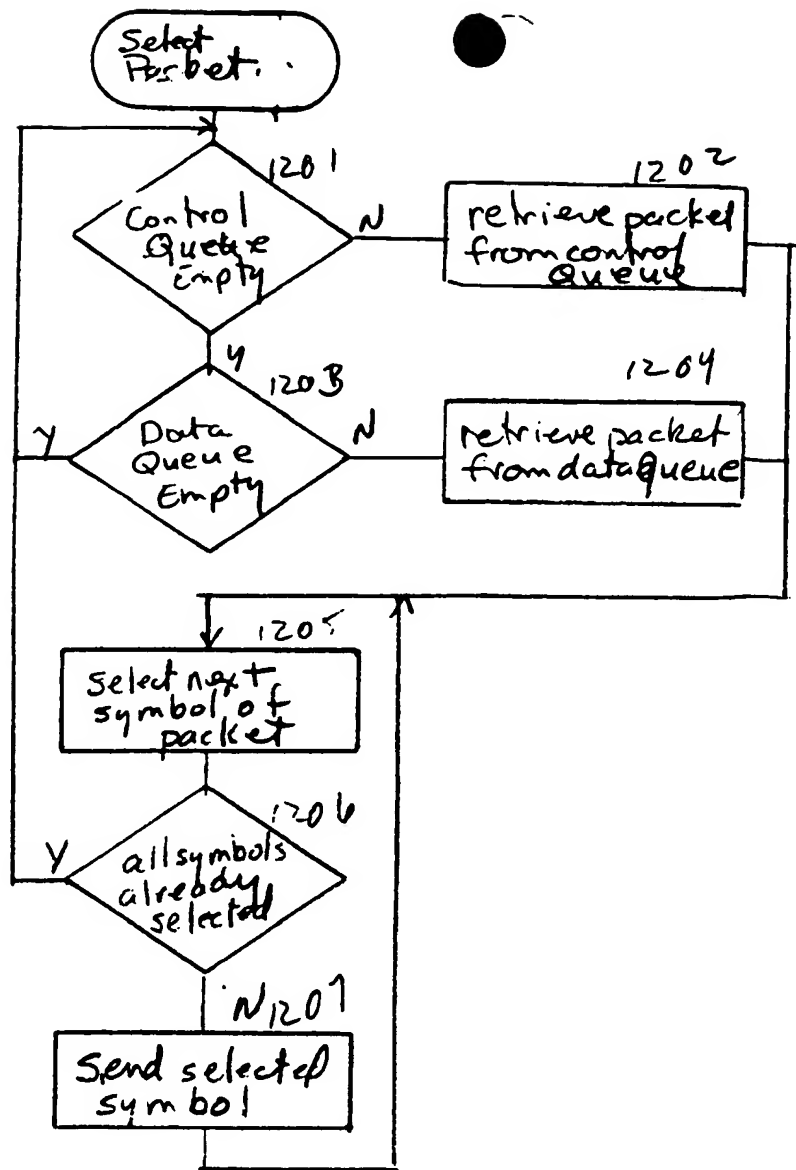


Fig 12

TOGETHER TOGETHER

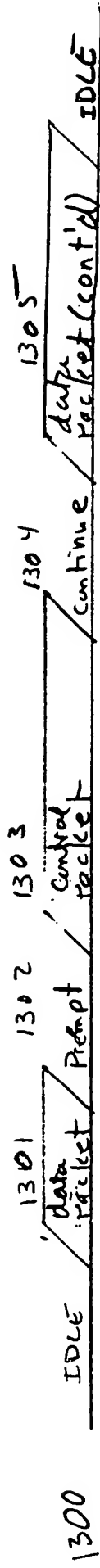


Fig 13

Send Packet
w Preemption

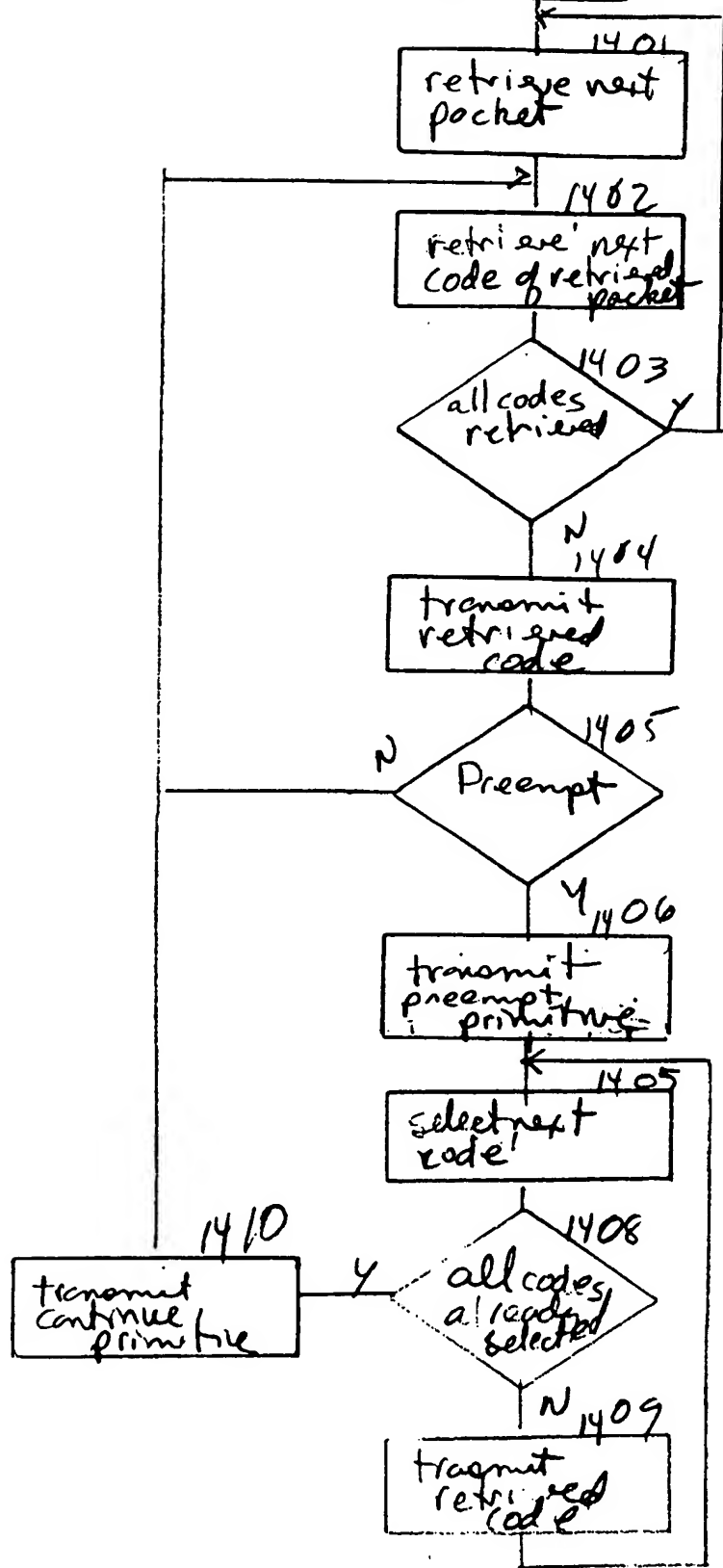


Fig 14

10045604 440701

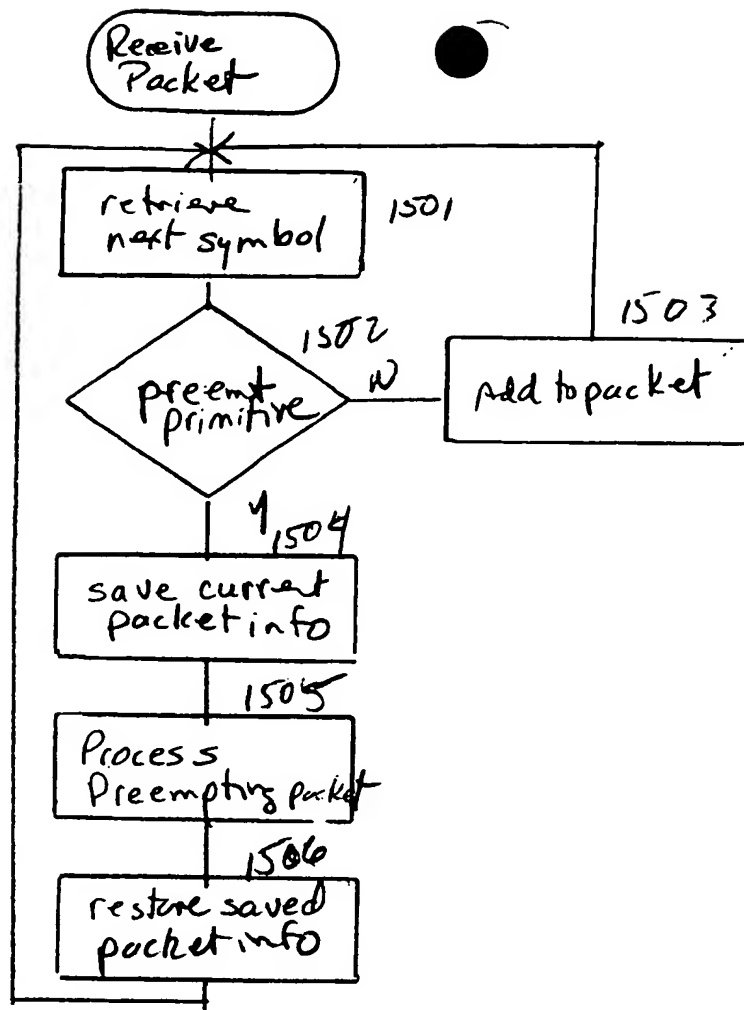


Fig 15

4045694.10704

FIG. 16

Switch Network

1630

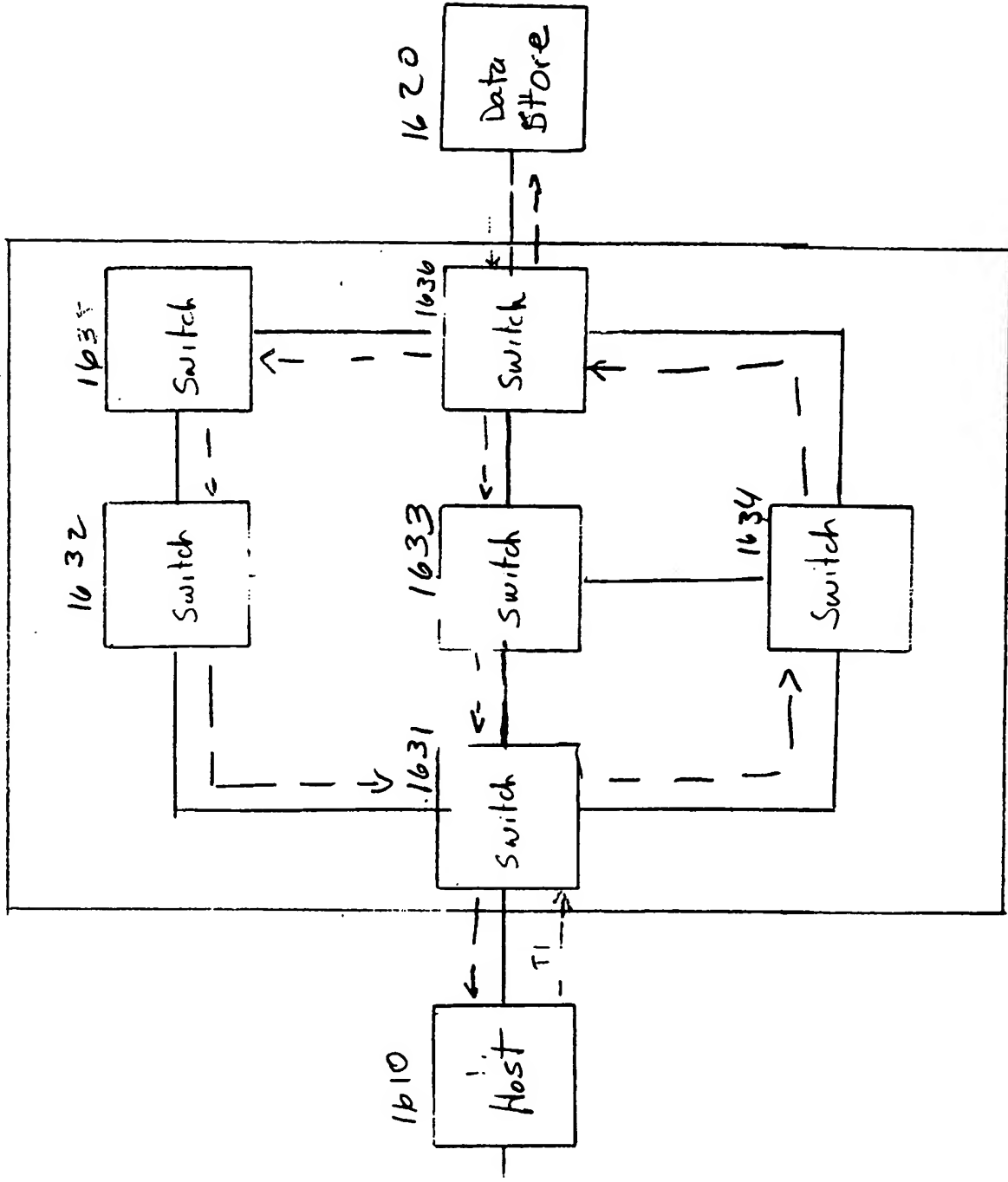
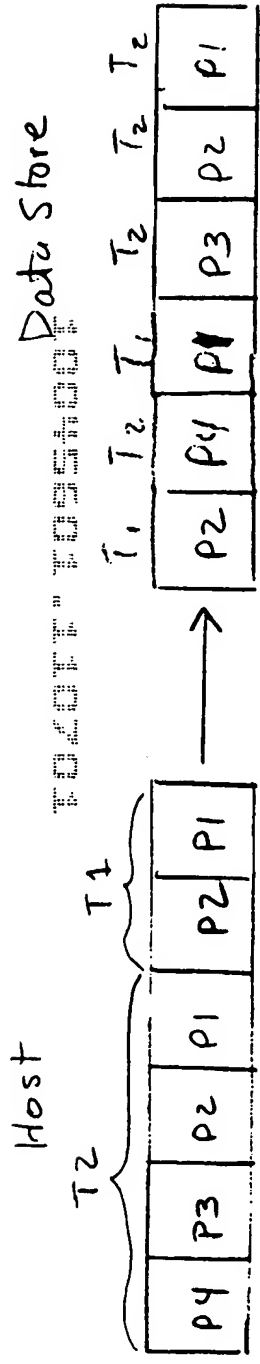


Fig. 16



Preserving Packet Order w/ Transaction

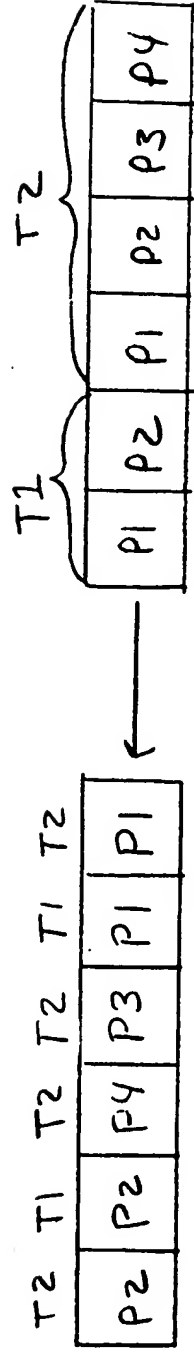


Fig 17

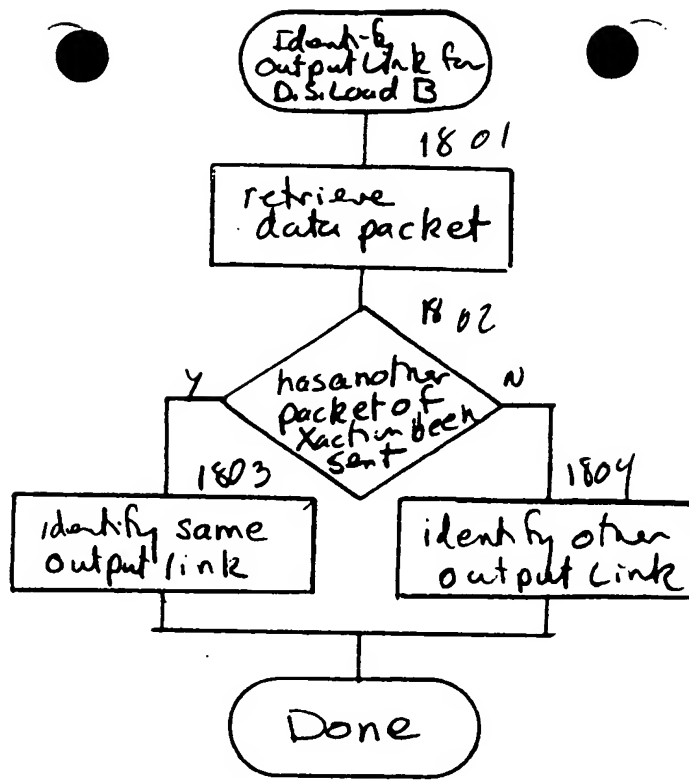


Fig 18

10015004 110704

FEDERAL BUREAU OF INVESTIGATION

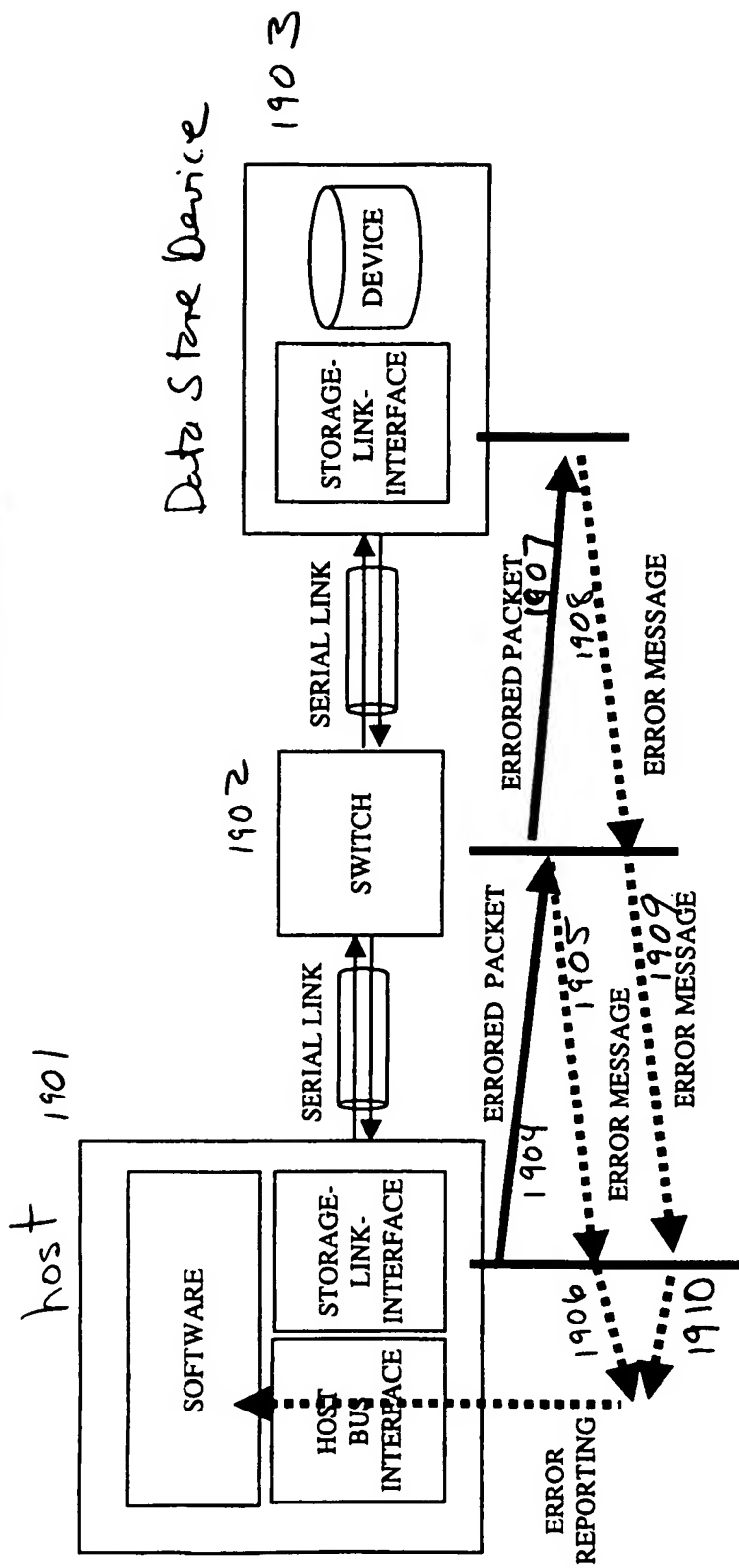
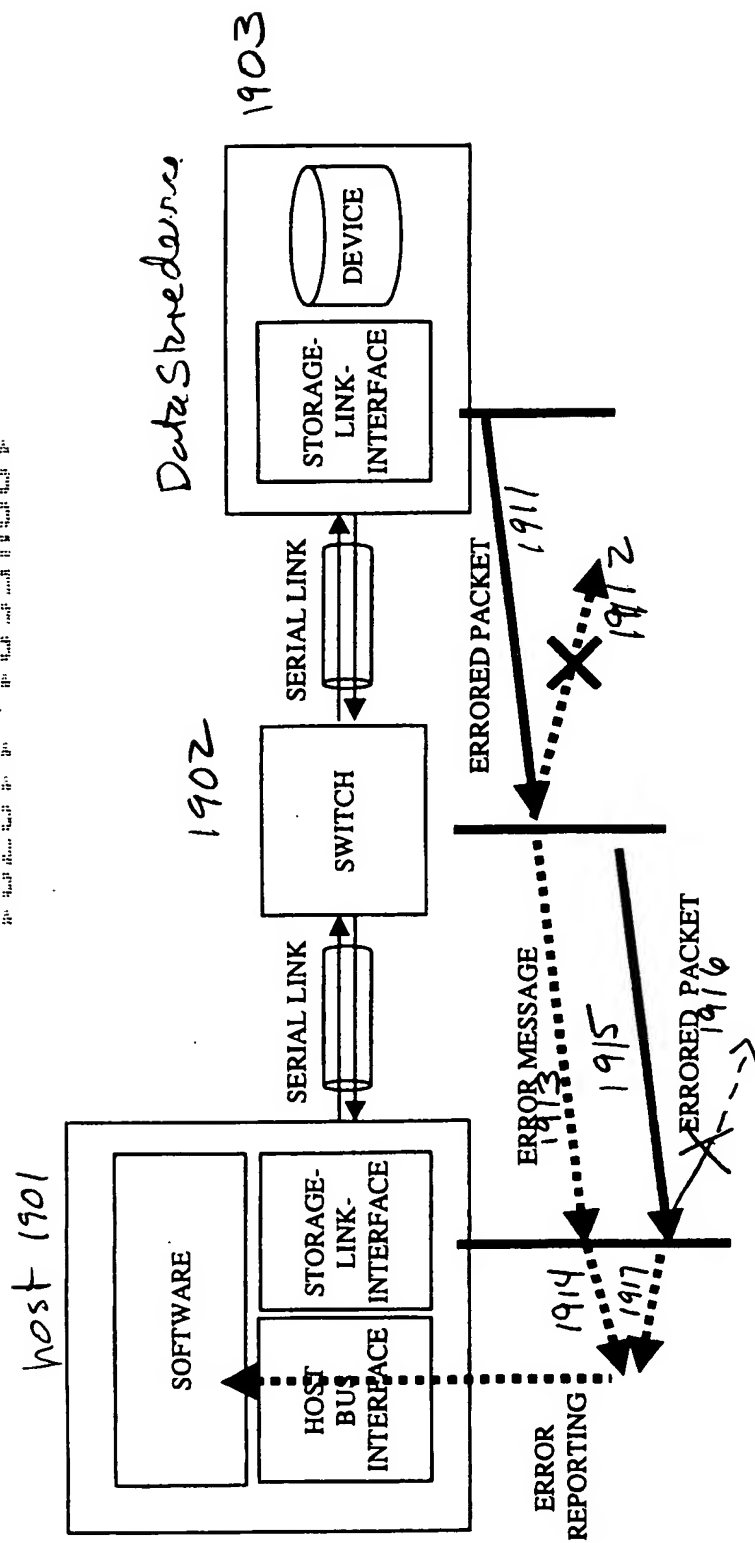


Fig 19A

100

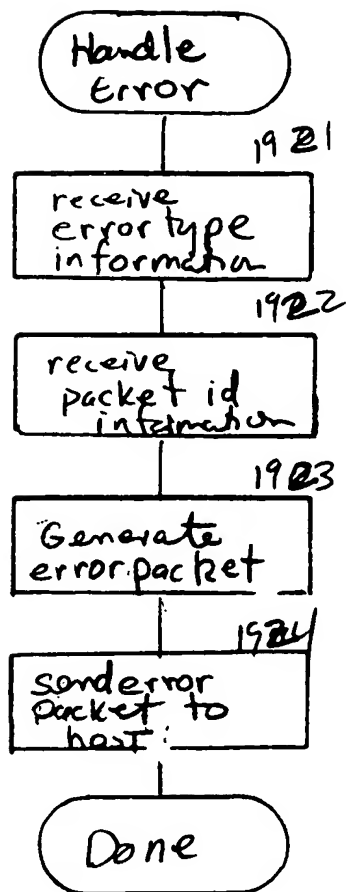
SECRET



19B

0202

10045604 110704
T000T000T000



19C

| 8b Code | 9 bit symbol |
|-----------|--------------|
| 0000 0000 | 101010101 |
| 0000 0001 | 101010100 |
| 0000 0010 | 101010111 |
| ⋮ | |
| 0101 0101 | 001010101 |
| ⋮ | |
| 0111 0110 | 001110110 |
| 0111 0111 | 100100010 |
| ⋮ | |
| 1111 1111 | 110101010 |

Fig 20

Fig 20 " 10704

TOGETHER TOGETHER

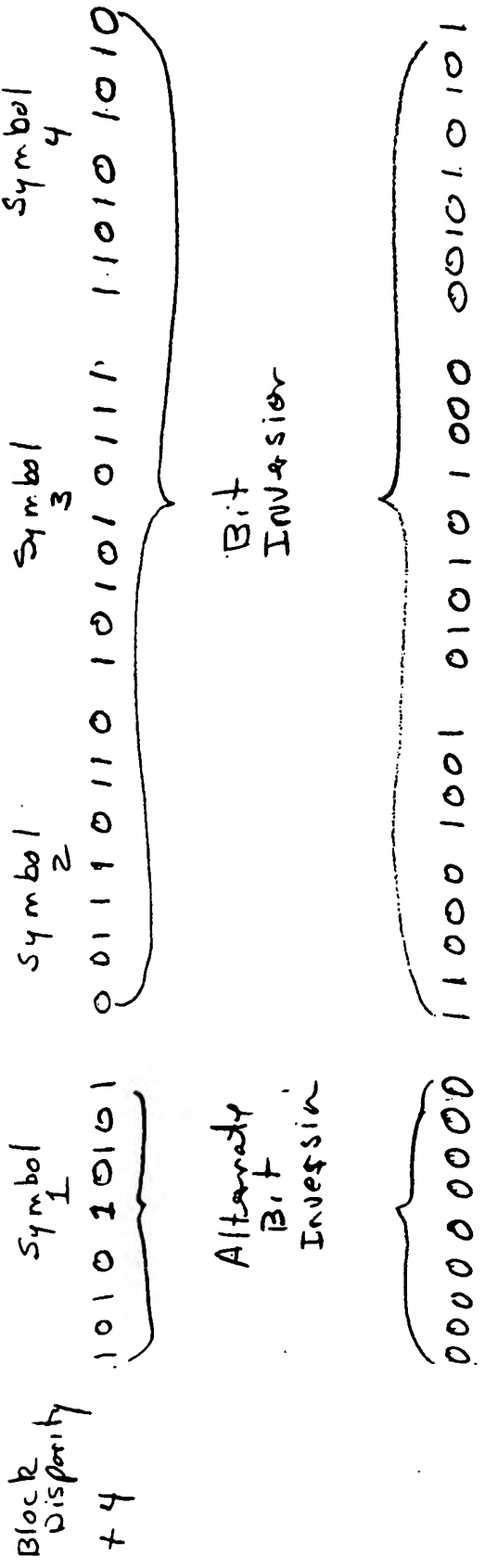


Fig 21A

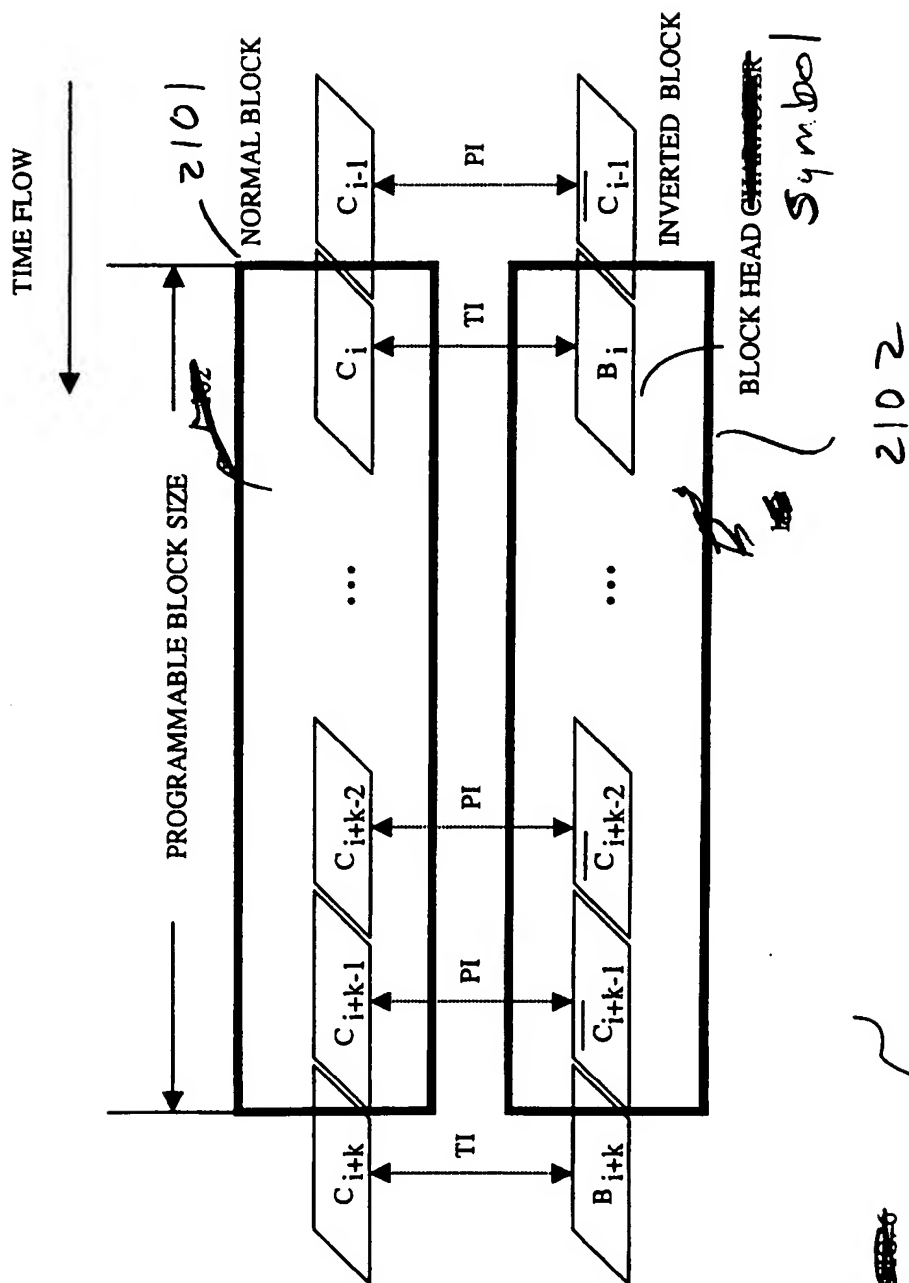


Fig 21B

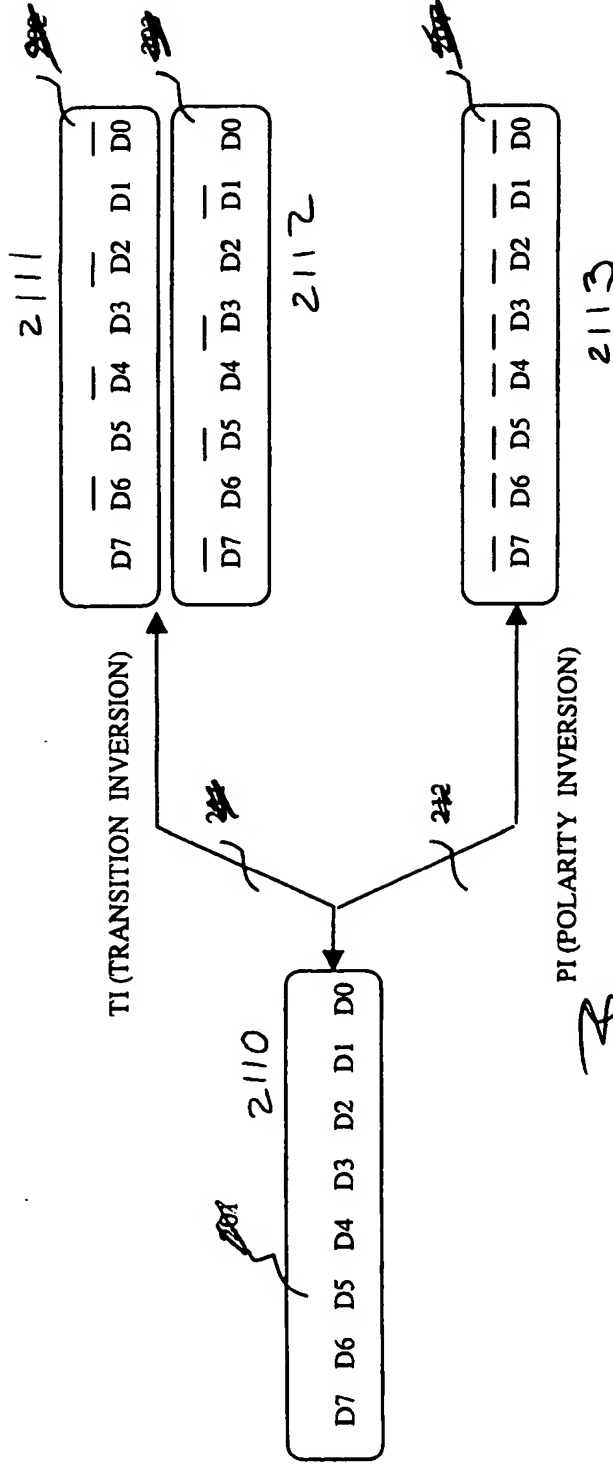


Fig 21C

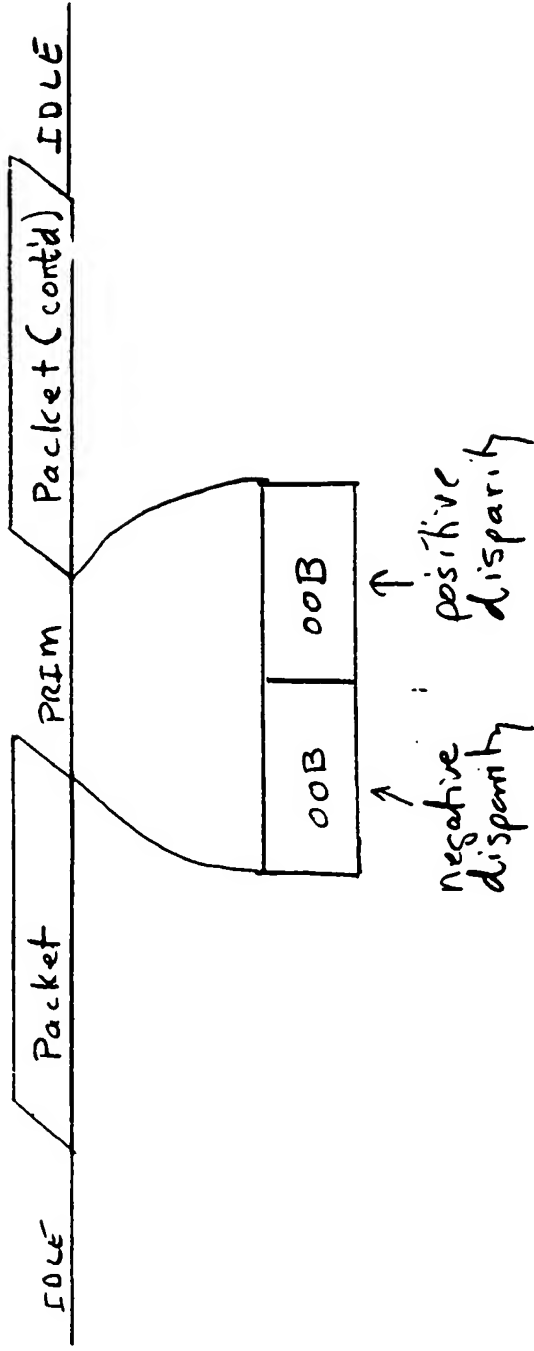


Fig 22

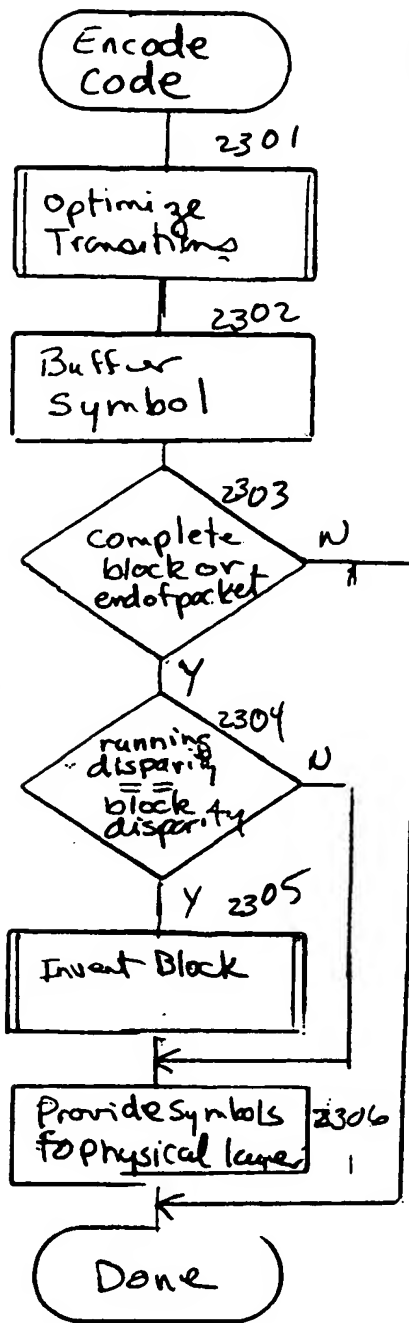


Fig 23

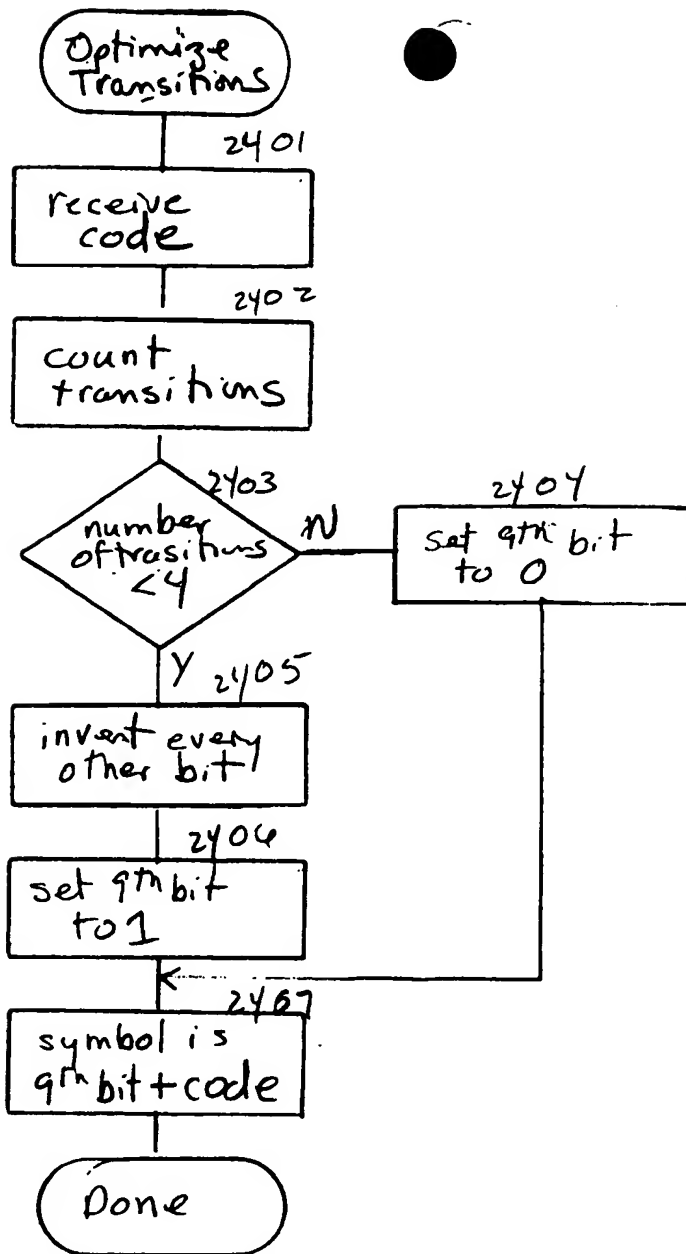


Fig 24

10045601-10204
T020T T005T001

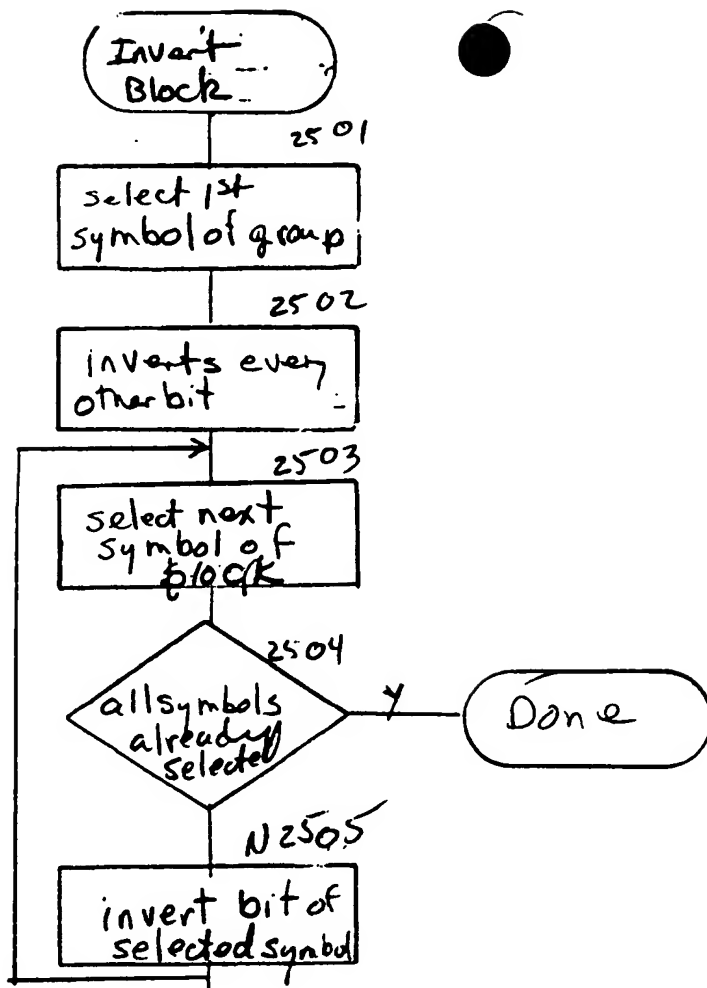


Fig 25

10045601 110701

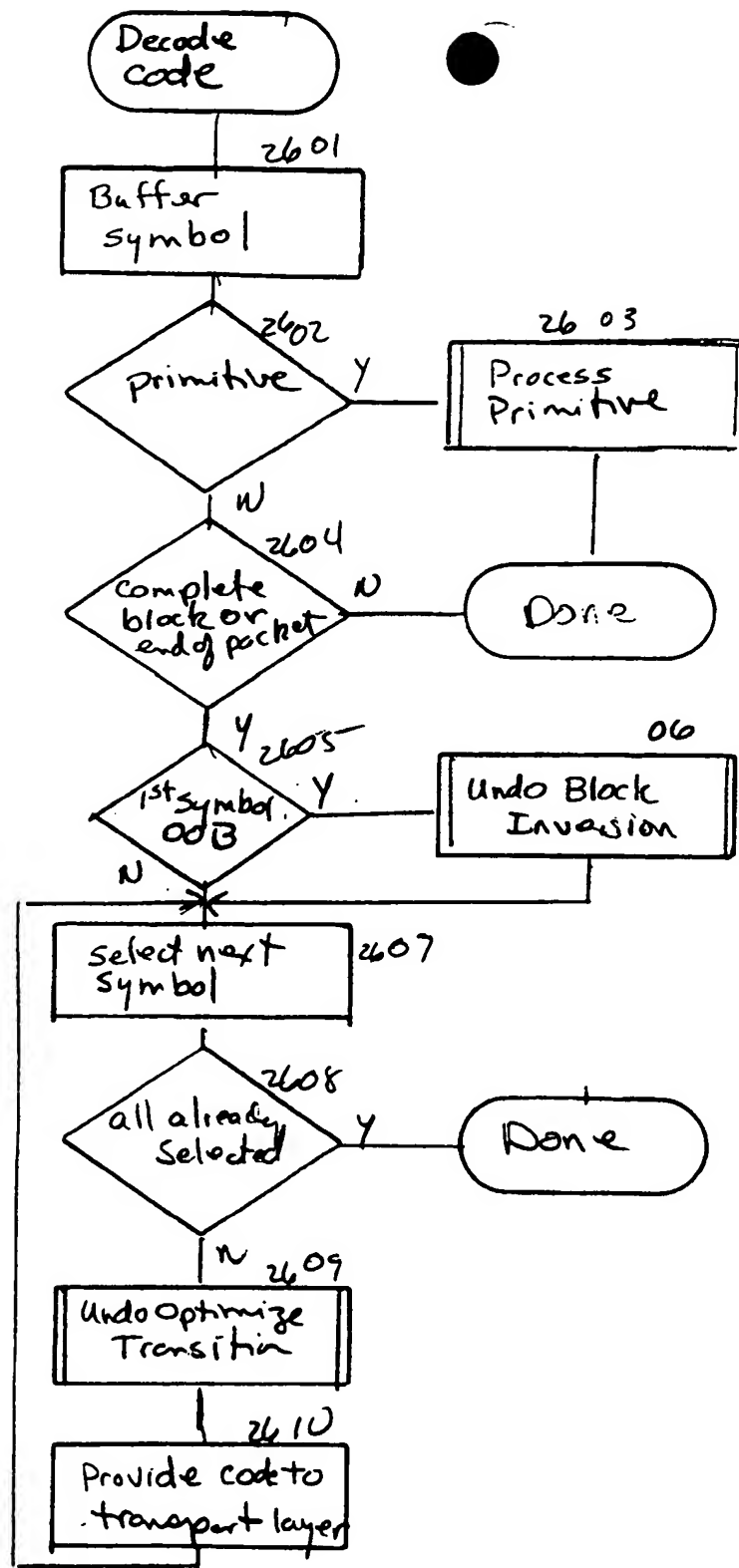


Fig 26

10045604 440704
T020T T05400

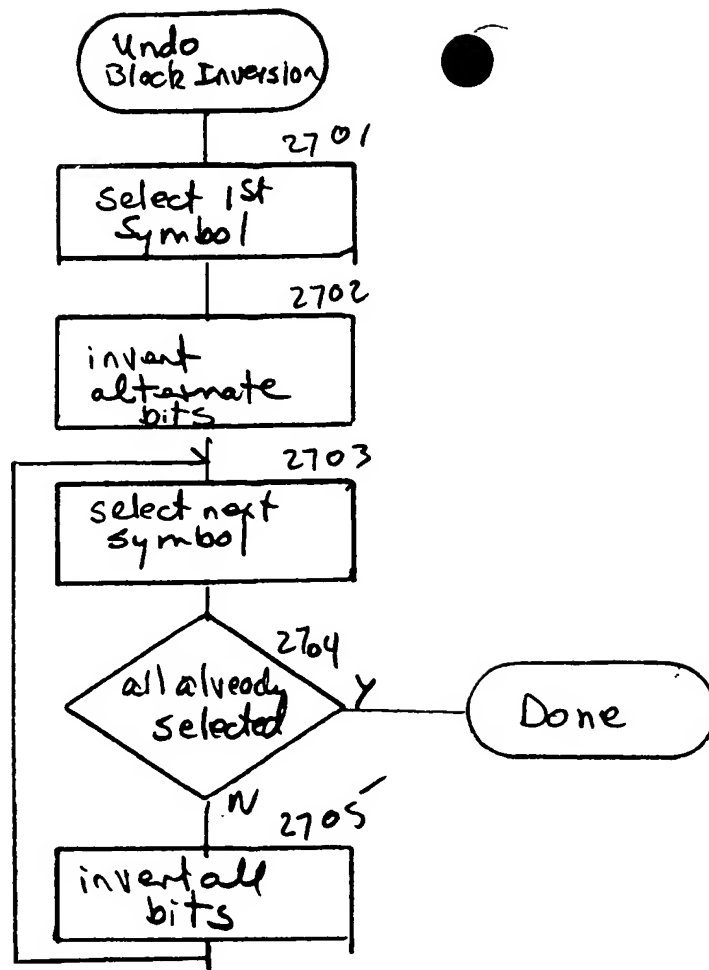


Fig 27

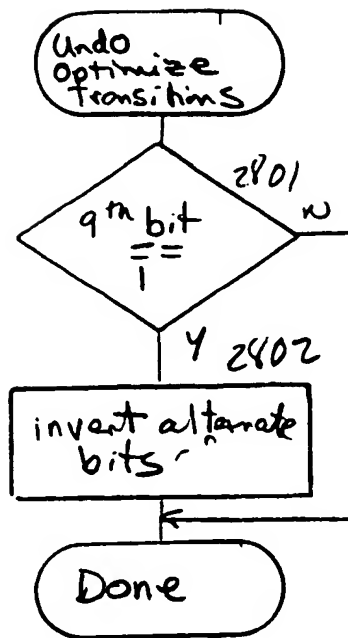


Fig 28

10043001 10204

10045601 440701
102077 1095400

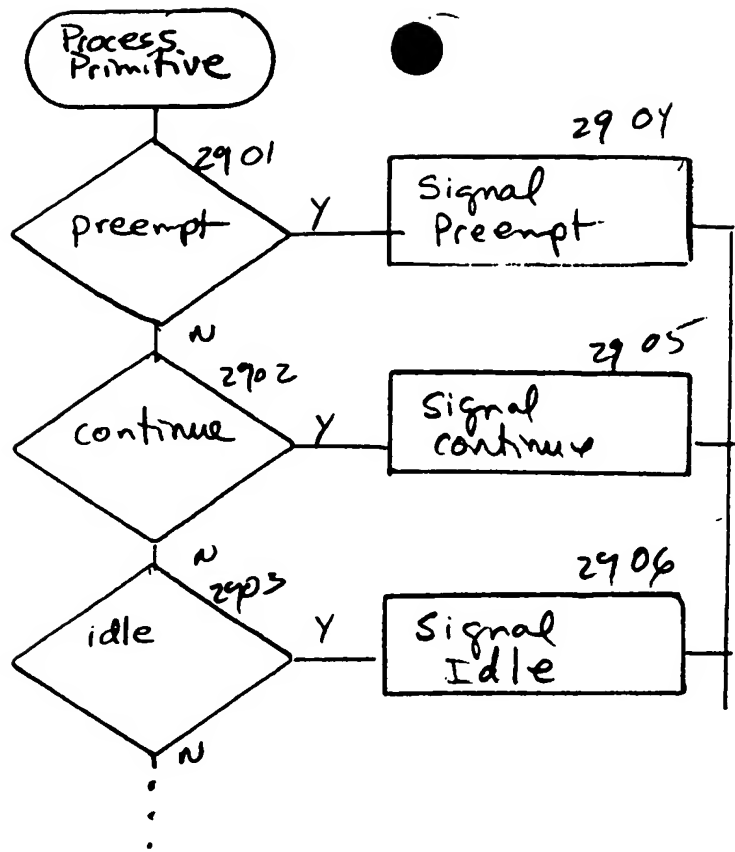


Fig 29

Multiport Memory Device 3000

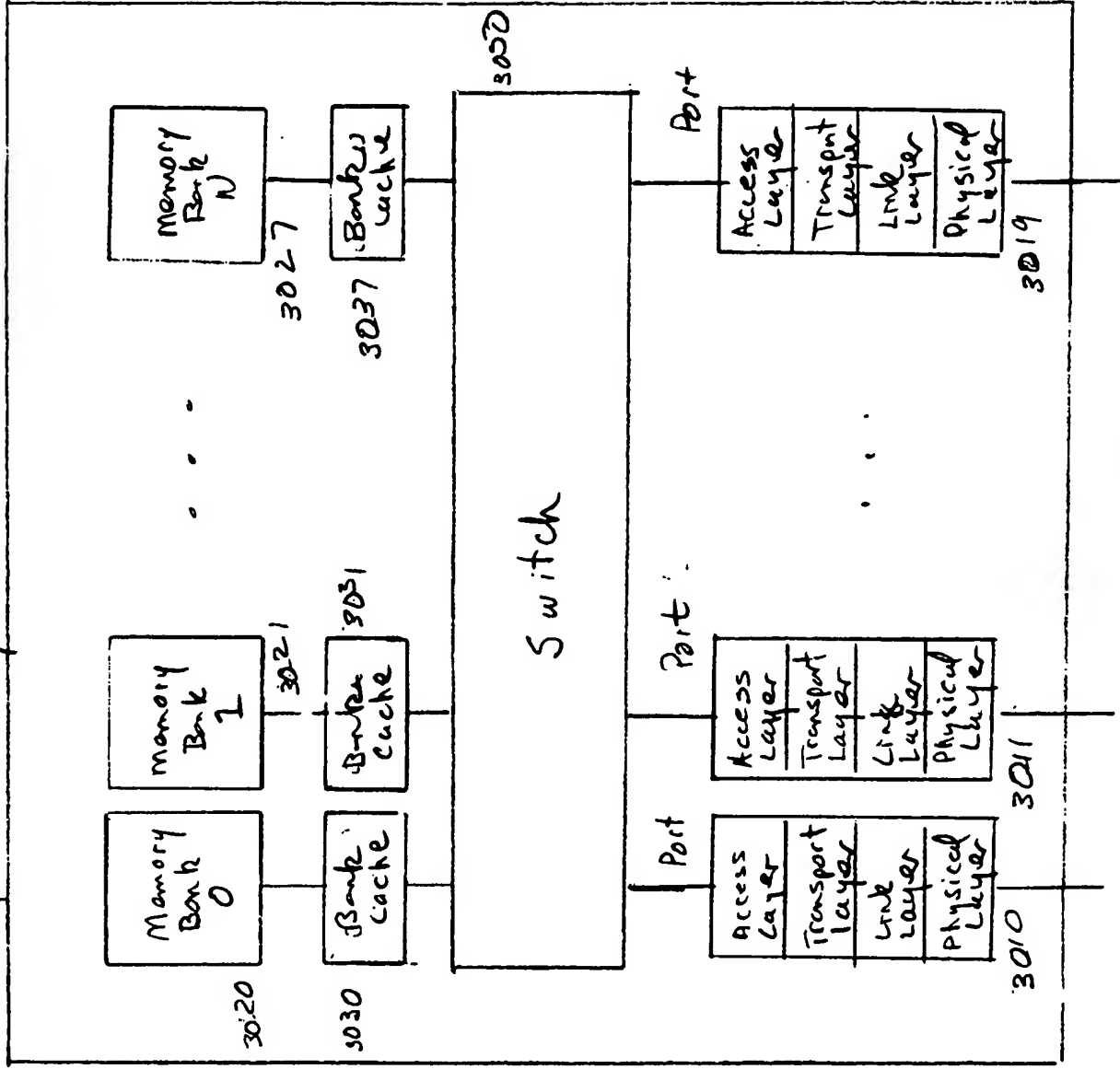


Fig 30

Physical Layer

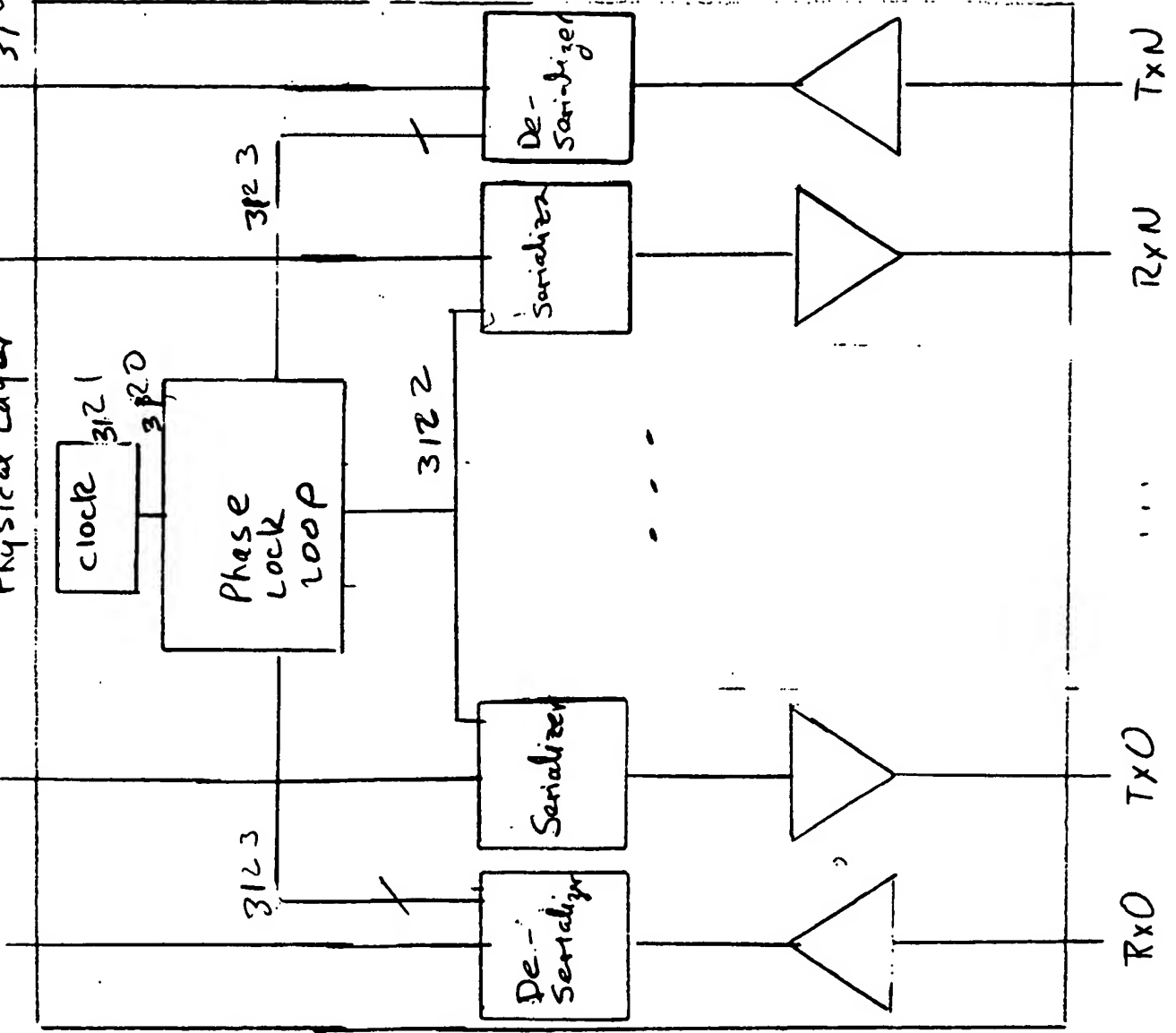


Fig 31

| Input Queue 3201 | | | Output Queue 3202 | | |
|------------------|-----|--------------|-------------------|------|---------|
| Port | R/W | Address Data | Valid | Port | Data |
| 3 | R | 1000 | 1 | 3 | 11...0 |
| 4 | W | 4000 | 0 | | |
| 3 | W | 1000 | 0 | | |
| 3 | R | 2000 | 1 | 3 | 101...1 |
| | | | | ⋮ | |
| | | | | | |

Fig 32

Access Layer (Receive)

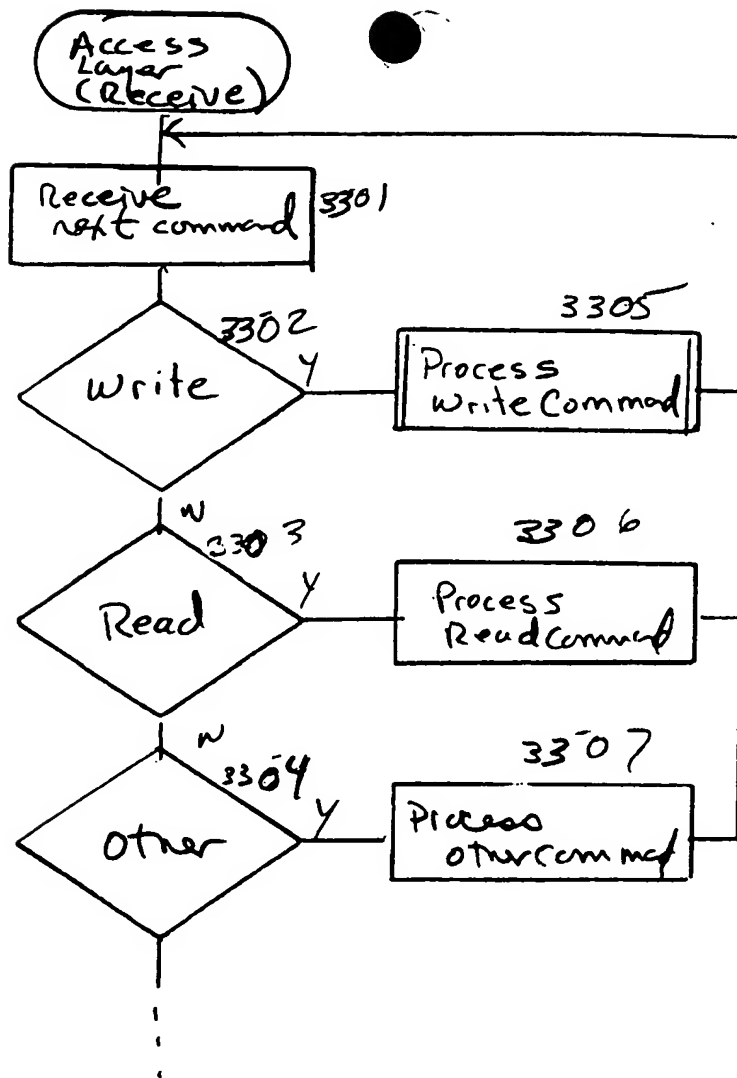


Fig 33

FIG. 34

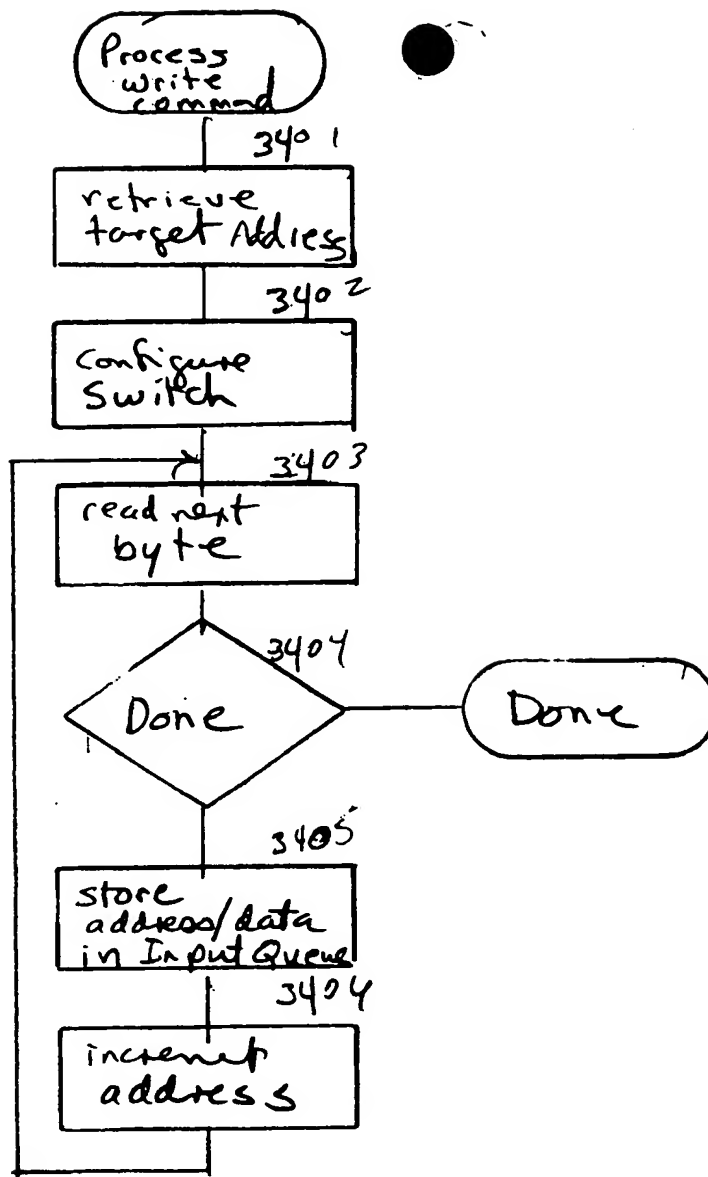


Fig 34

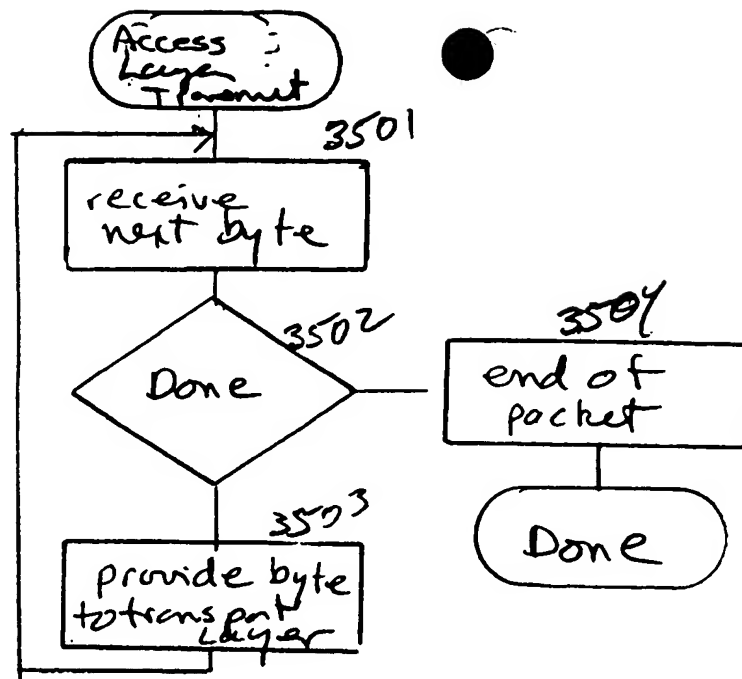


Fig 35

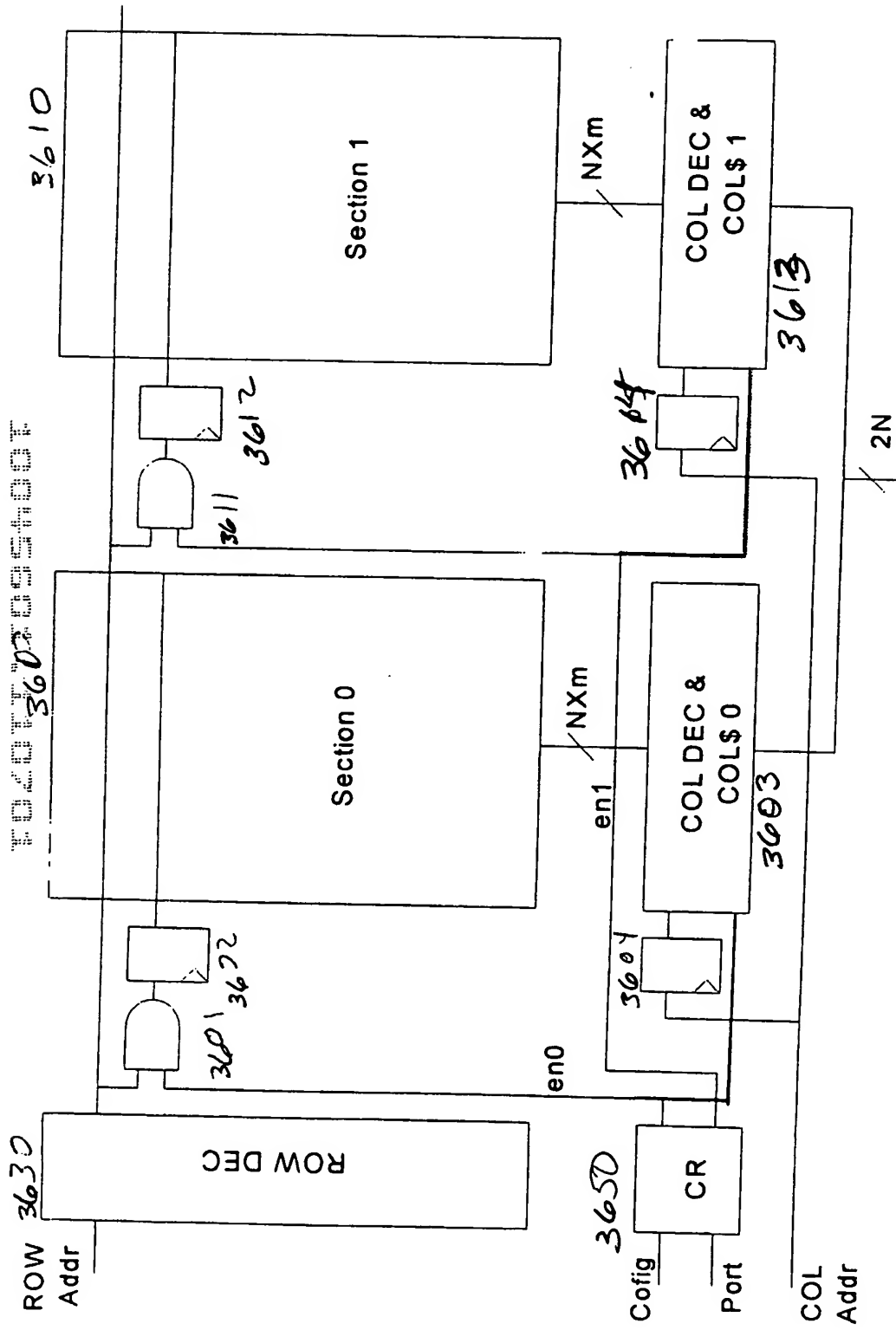
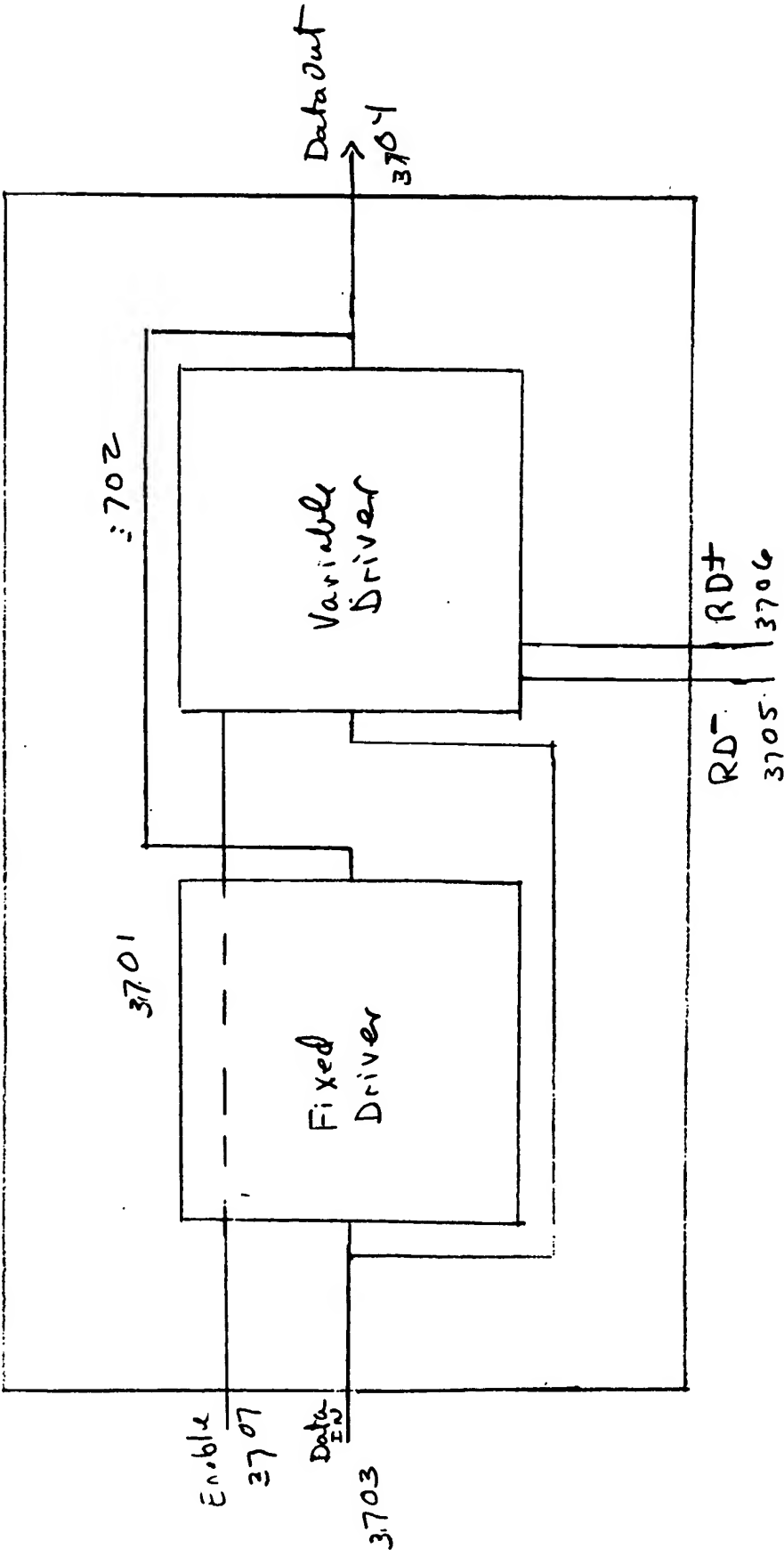


Fig 36

Line Driver 3700



Variable Driver

$\begin{cases} RD^+ \wedge \overline{DataIn} = \text{pull down} \\ RD^- \wedge DataIn = \text{pull up} \end{cases}$

Fig 37A

PORT ADDRESS

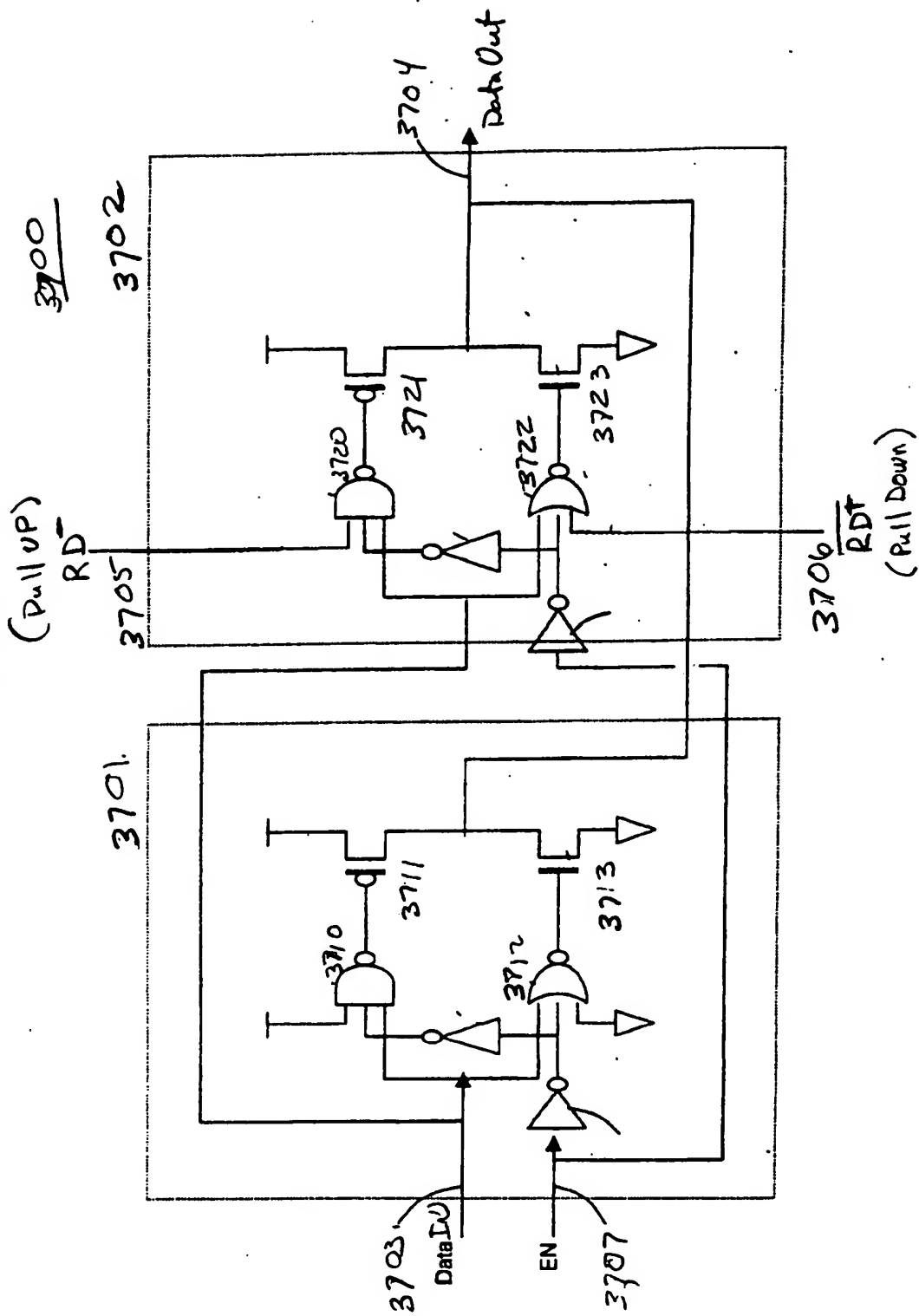


Fig 37B

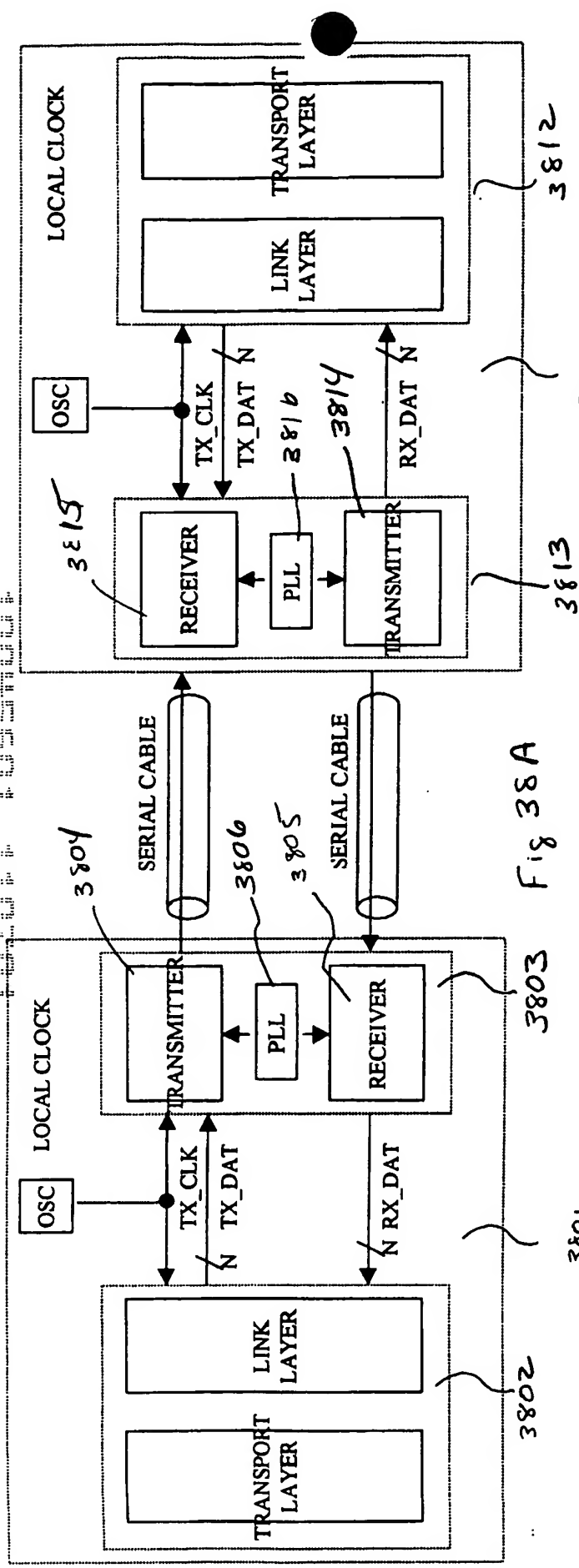


Fig 38A

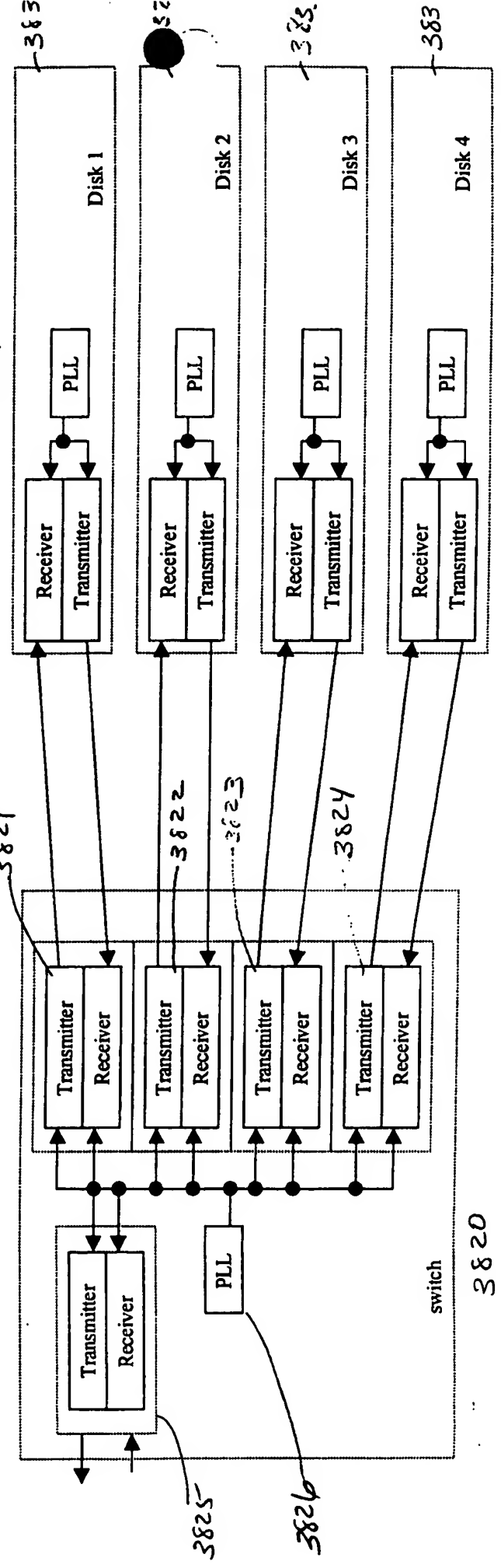


Fig 38B

FIG. 39A

3920

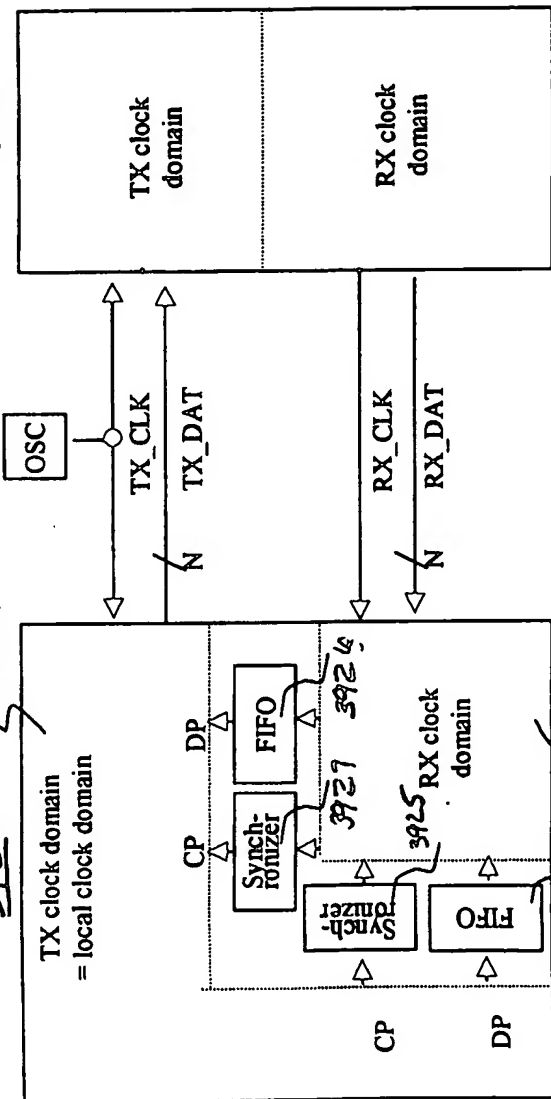


Fig 39A

3923 3924 3925 3926

3940

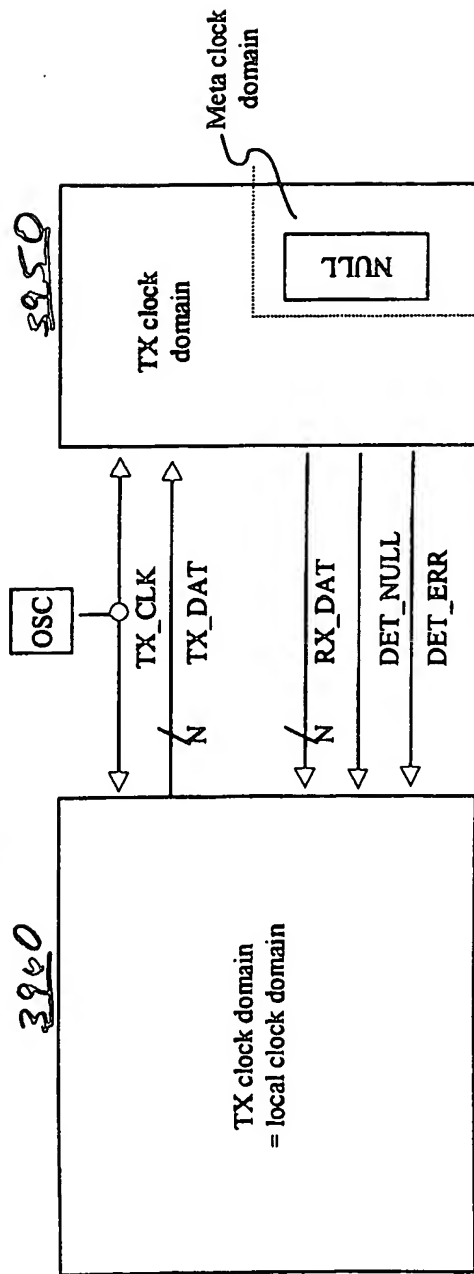


Fig 39B

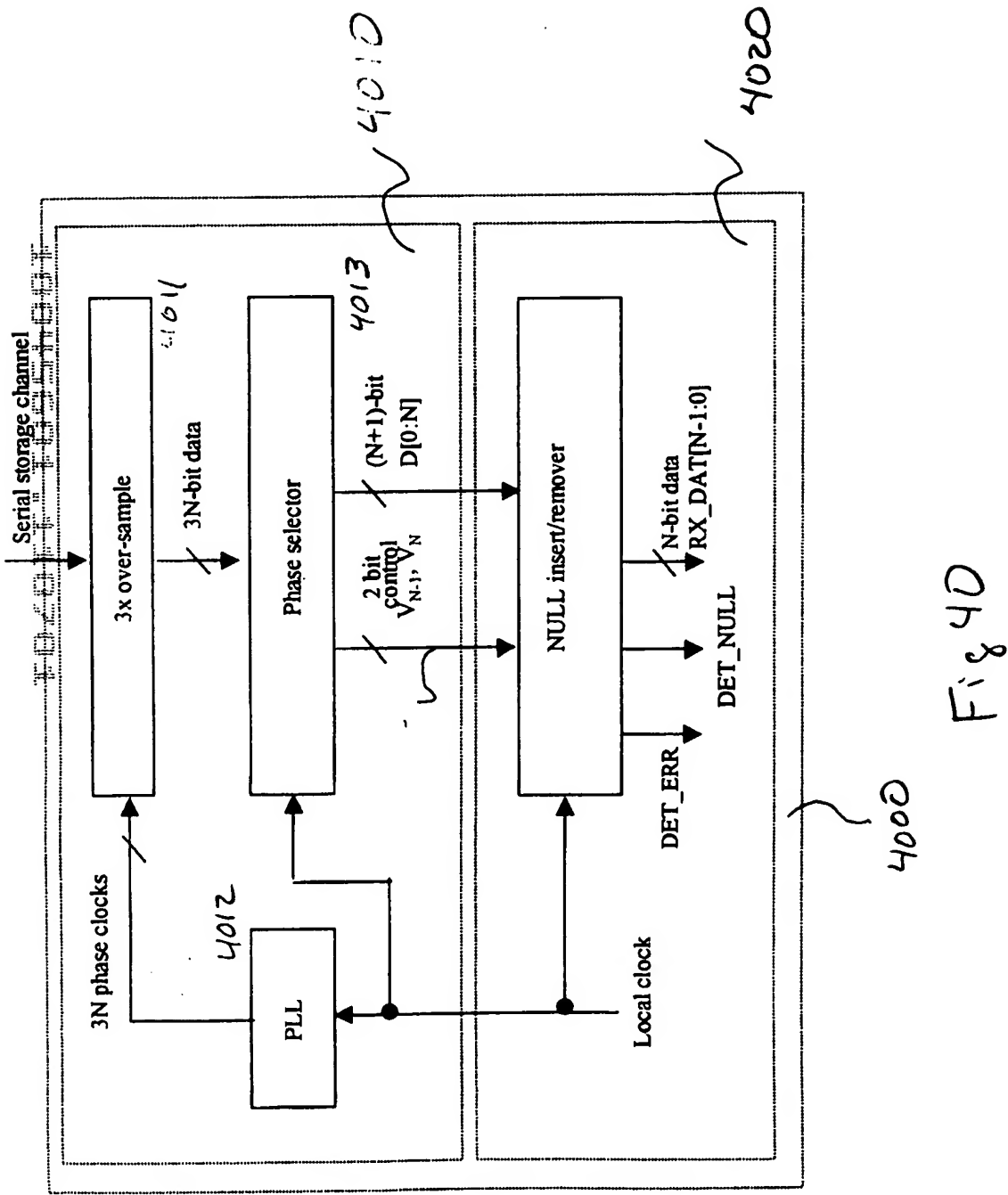


Fig 40

FIG. 41

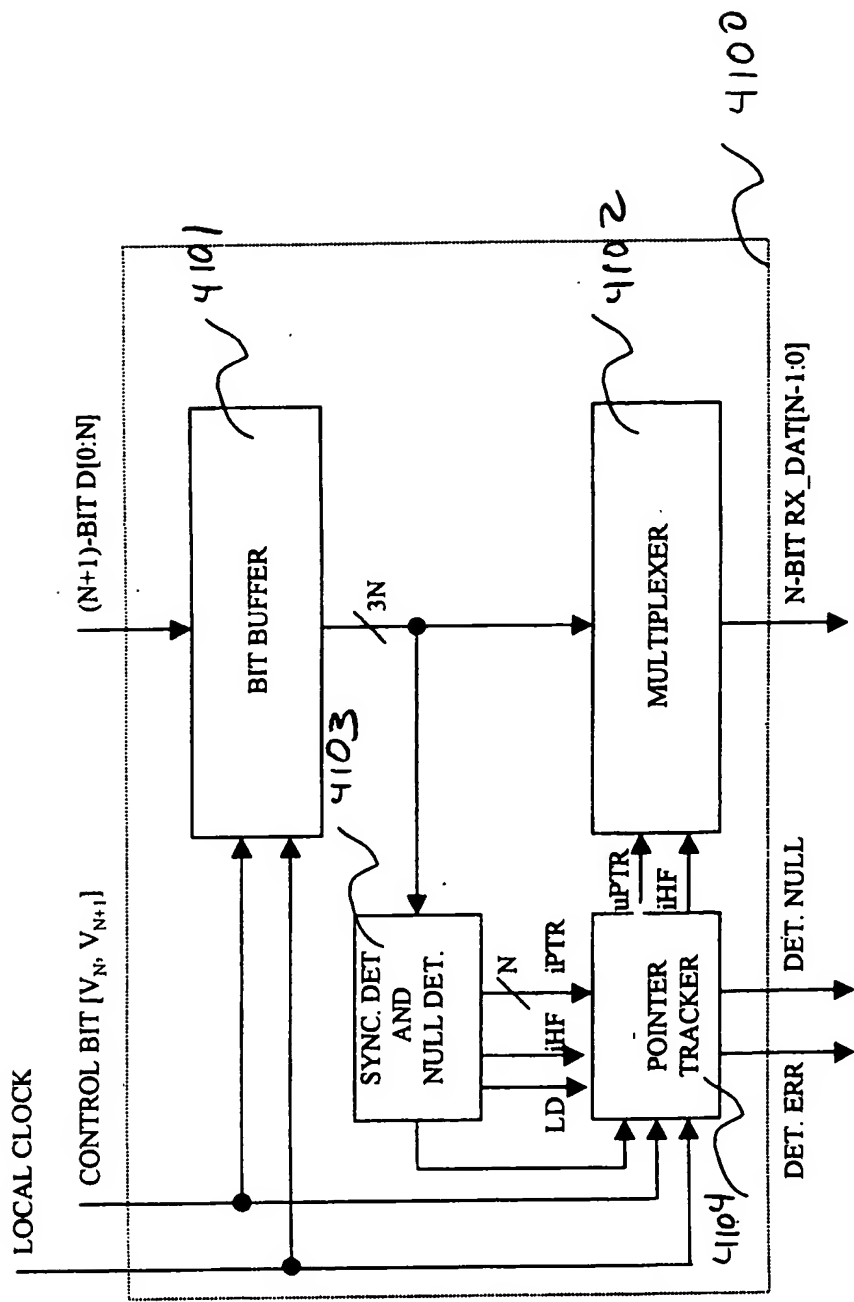


Fig 41

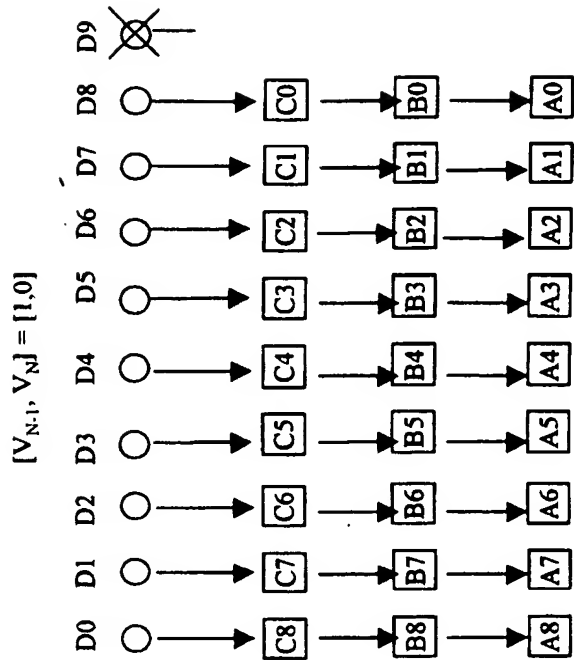


Fig 42A

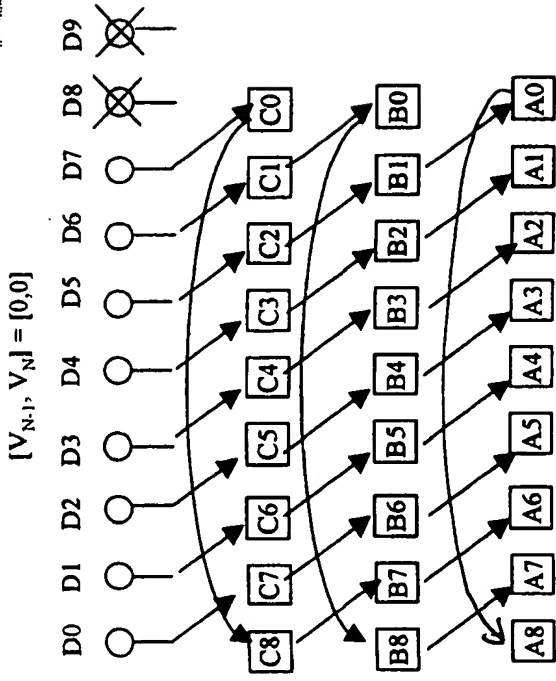


Fig 42B

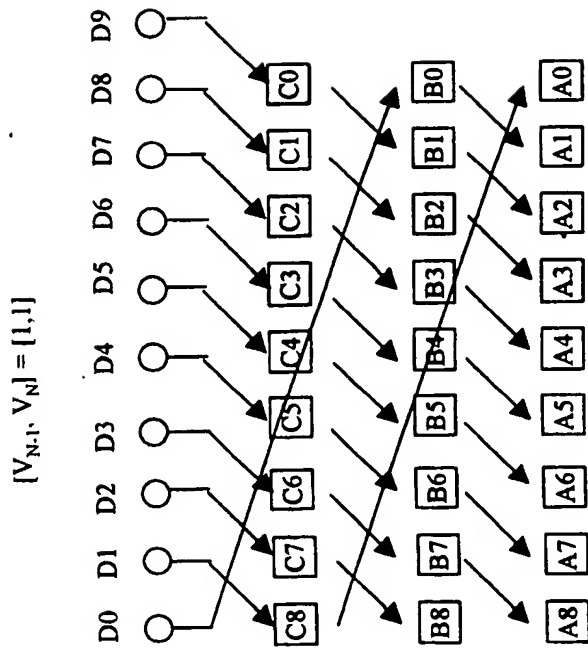
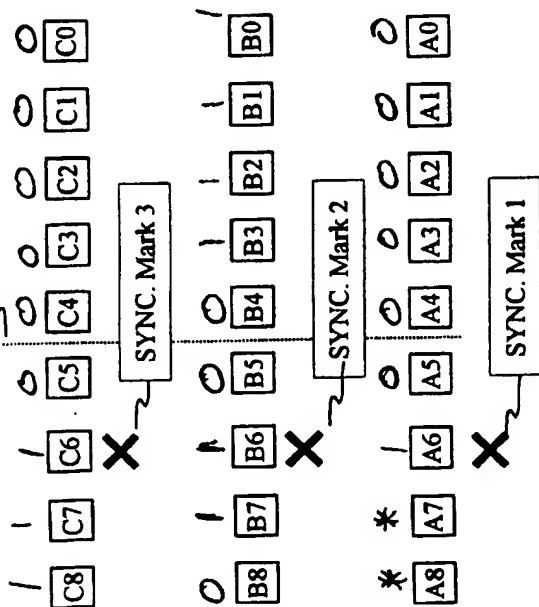


Fig 42C

4301

Half line

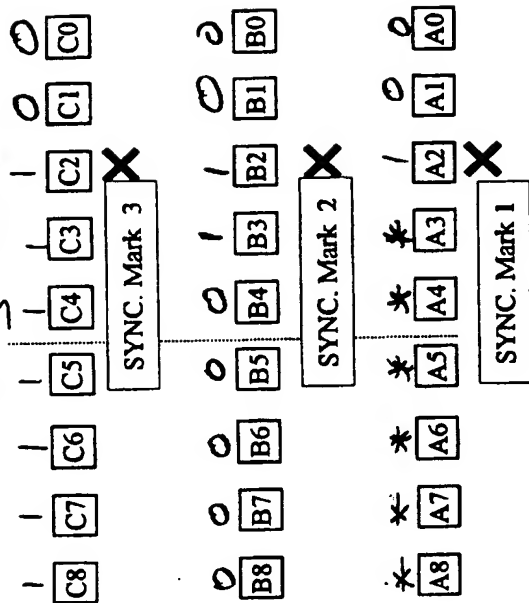


LD = 1, iHF = 0, iPTR = "001000000"

SYNC. Mark

4302

Half line



LD = 1, iHF = 1, iPTR = "000000100"

SYNC. Mark

Fig. 43

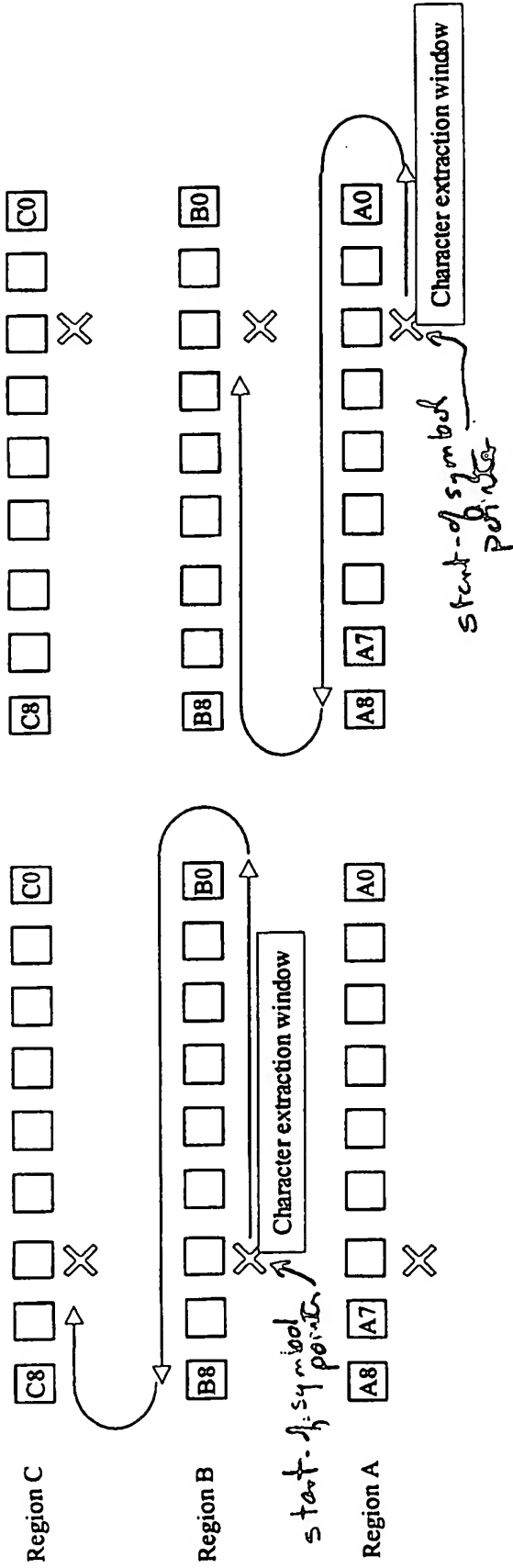


Fig 44

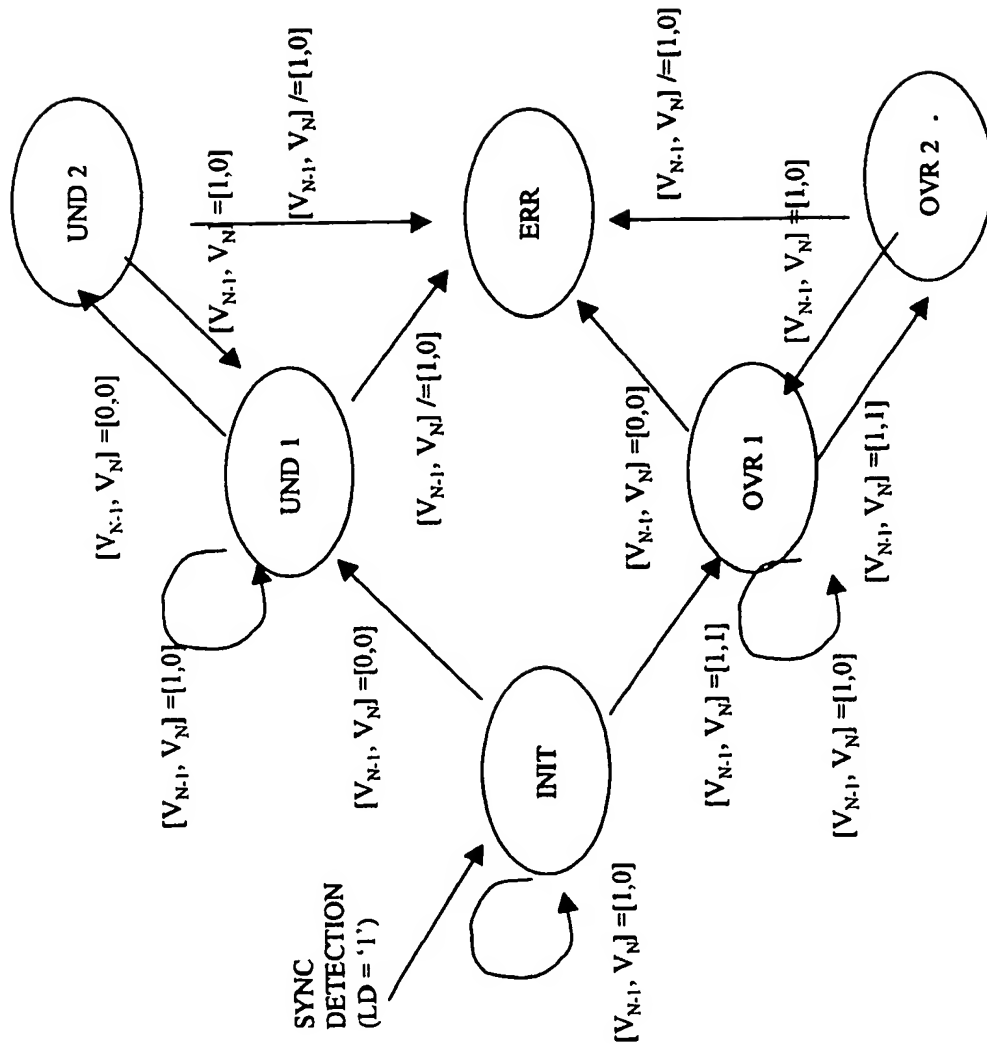


Fig 45

FD40F FD95400F

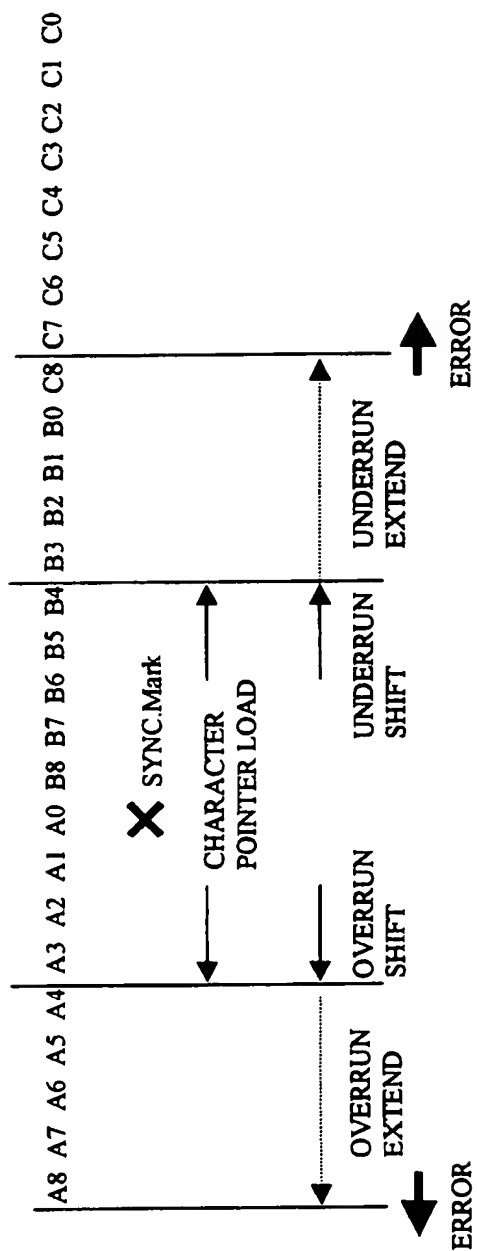


Fig 46

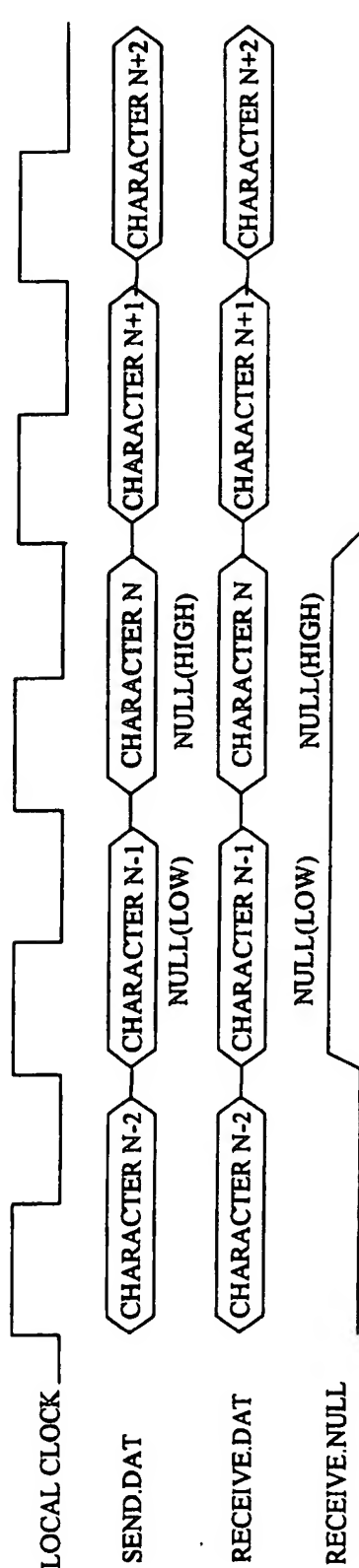
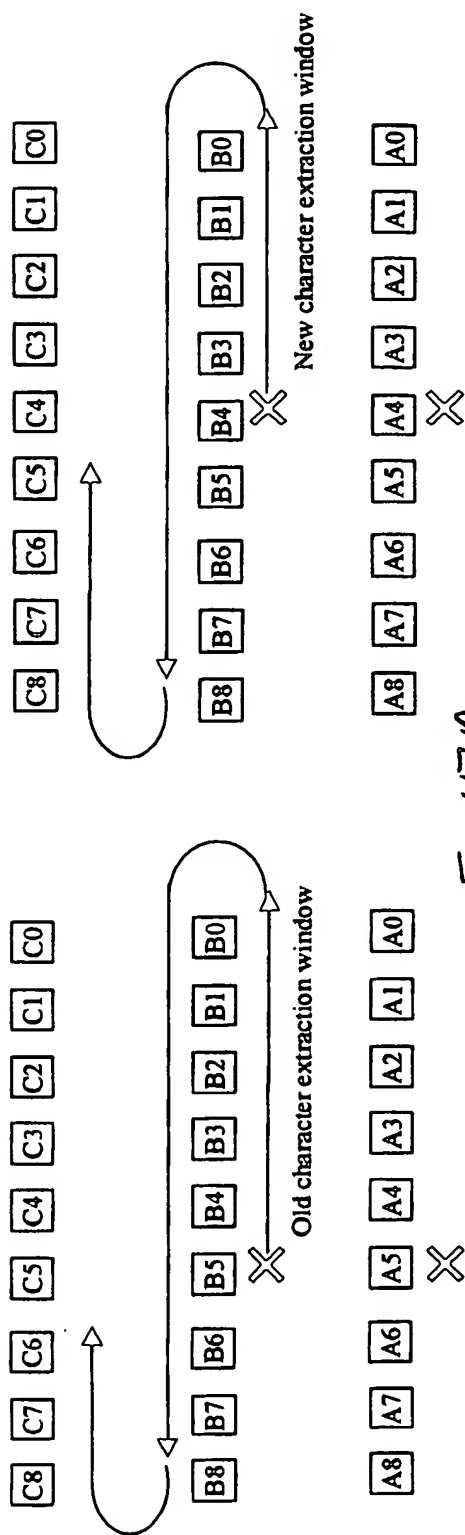


Fig 47B

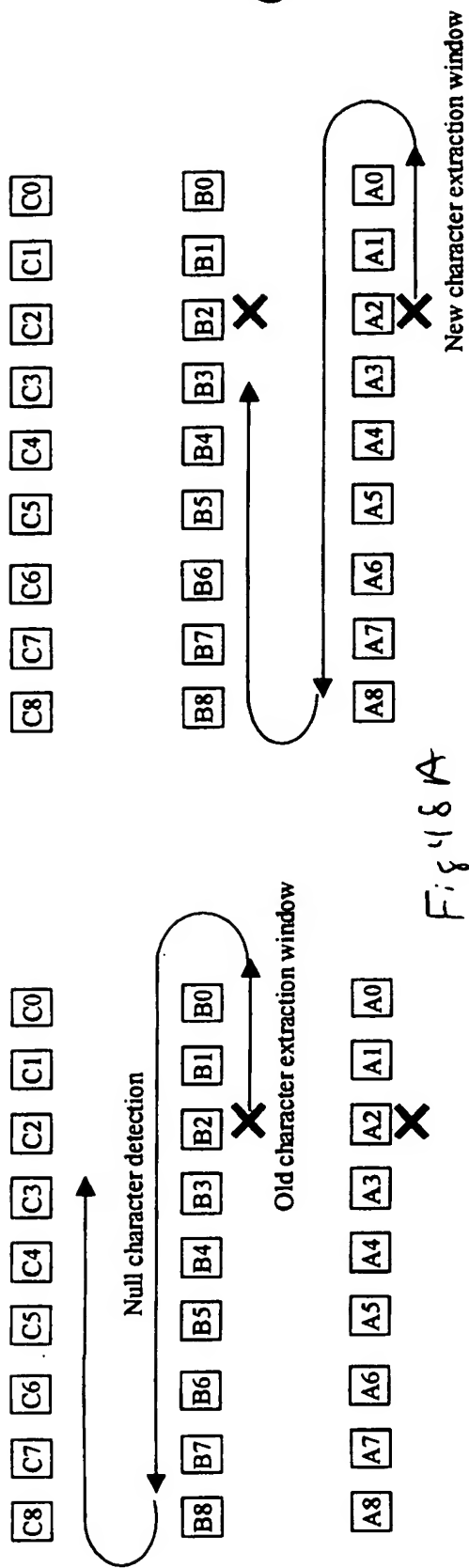


Fig 48A

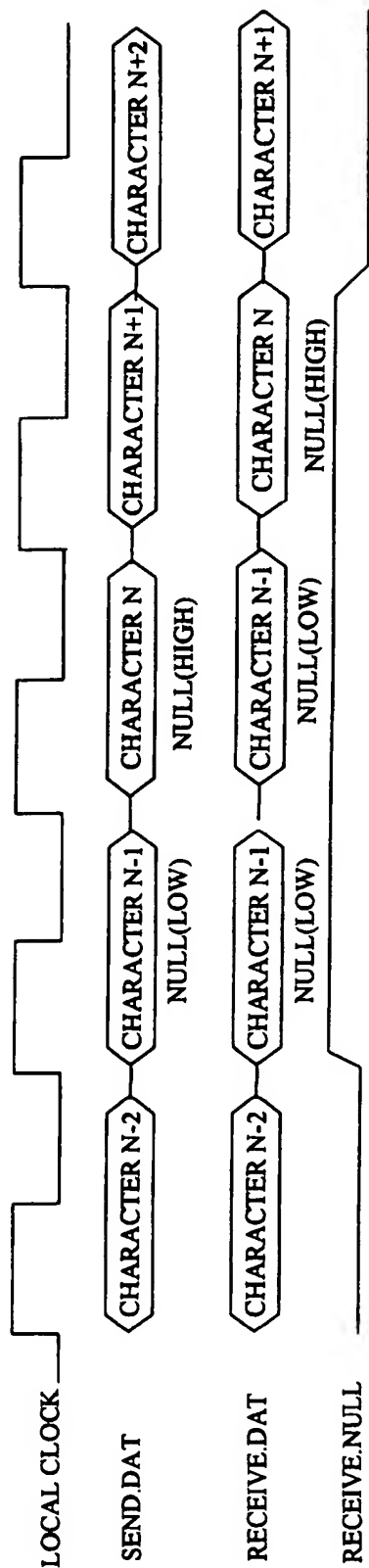


Fig 48B

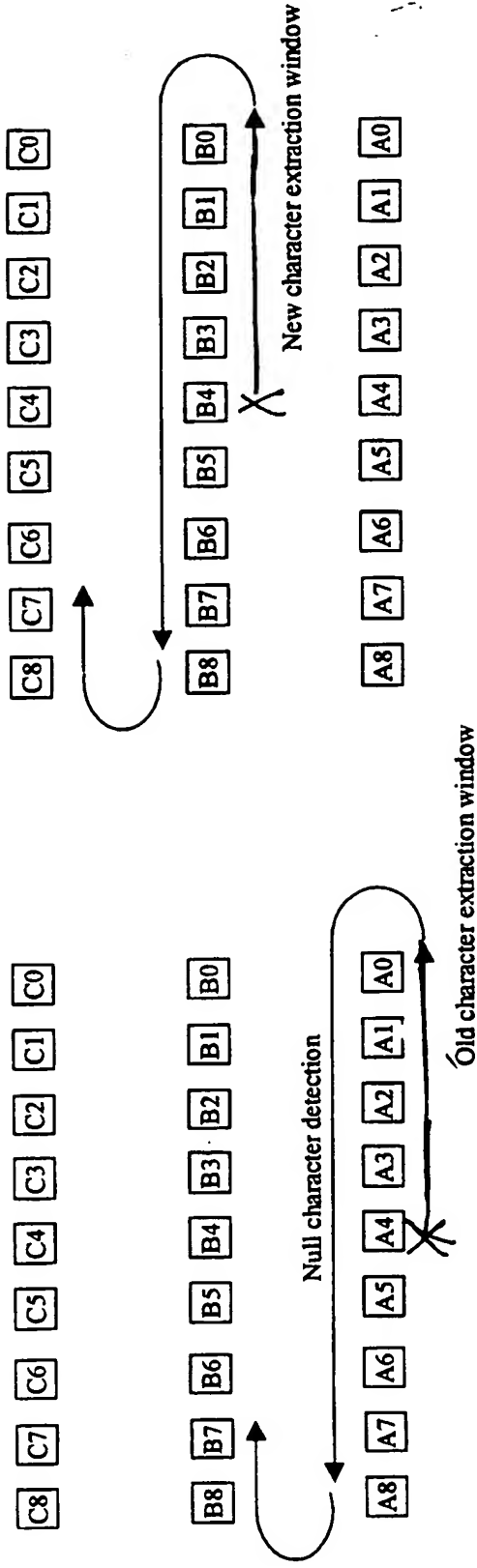


Fig. 49A

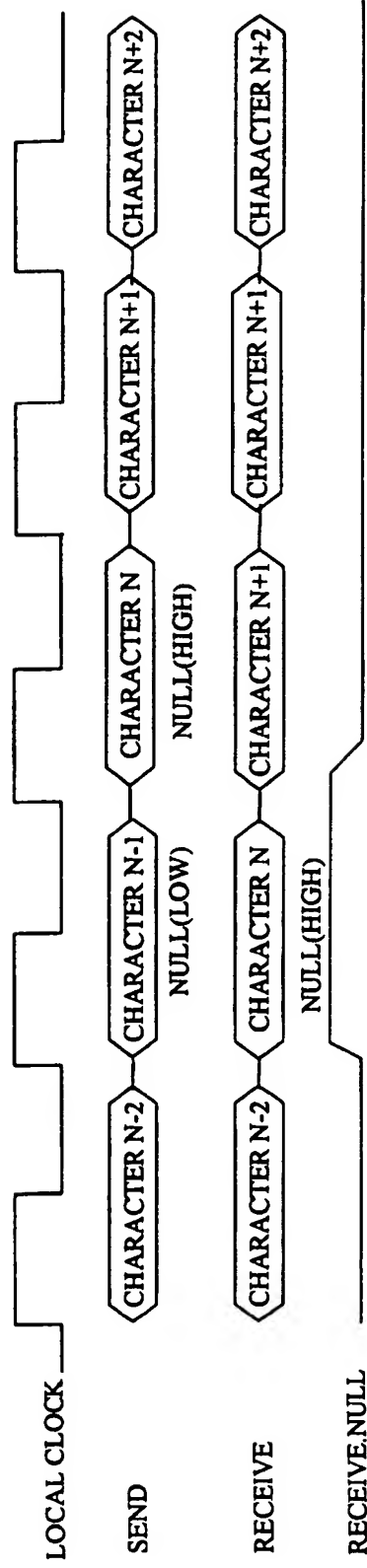


Fig 49B